#15

I hereby certify that this correspondence is being facsimile transmitted to the U.S. Patent and Trademark Office, PCT Legal Office, on the date shown below.
Attention: Leonard Smith, PCT Legal Examiner
Anthony Smith, Attorney-Advisor

Facsimile: 703-308-6459
Pages - 5 Pages
Date: August 26, 2002

Diane Schwaiger
Diane Schwaiger

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Ali Jalali, Pierre Leray, and Dominique Lacroix

Art Unit: Not Assigned

Application No.: 09/581,272

Examiner: Not Assigned

Date Received by PCT/PTO: June 7, 2000

Docket No.: 136.147
Customer No. 023907

For: *Method of Calculating the Fast Fourier Transform and the Inverse Fast Fourier Transform*

RENEWED PETITION UNDER 37 CFR 1.47(a)

Assistant Commissioner for Patents
Box PCT
Washington, D.C. 20231

Attention: Mr. Leonard Smith, PCT Legal Examiner
Mr. Anthony Smith, Attorney-Advisor

Dear Messrs. Smith and Smith:

The *Decision on Petition Under 37 CFR 1.47(a)* mailed June 25, 2002 states that item (2), factual proof that the missing joint inventor refuses to execute the application or cannot be reached after diligent effort, has not been satisfied. Otherwise, all other items have been satisfied.

In the Decision, the examiners state that "a copy of the certified mail return receipt postcard has not been included as an exhibit to the present petition." Applicants' representative submits that the original certified mail return receipt postcard showing that the February 1, 2001

February 1, 2001 letter to Mr. Jalali was received was attached to the *Declaration and Statement of Facts by Christian Hamon in Re 37 CFR 1.47(a) and Section 409.03(d) MPEP* that was filed with the *Renewed Petition Under 3 CFR 1.47(a)* on April 11, 2002. A photocopy of the certified mail return receipt postcard is enclosed herewith. Also enclosed is a copy of our postcard showing that the original certified return receipt postcard was received by the PCT/PTO on April 23, 2002.

As Mr. Hamon stated in the *Declaration and Statement of Facts by Christian Hamon in Re 37 CFR 1.47(a) and Section 409.03(d) MPEP*, filed on April 11, 2002, he could not identify the signature of the person who signed the return receipt postcard.

Forthcoming Evidence

As documentary evidence that every effort has been made to obtain Mr. Jalali's signature on the application papers an additional Declaration and Statement of Facts with copies of the following documents will be filed as soon as the executed Declaration is received from Mr. Hamon:

1) French-language letter addressed to Mr. Jalali dated September 28, 2000, an English translation of the letter, a Declaration and an Assignment that was enclosed with the letter, and the address label;

2) French-language letter addressed to Mr. Jalali dated February 1, 2001, an English translation of the letter, a Declaration and an Assignment that was enclosed with the letter, the address label, and the return receipt postcard signed by a person whose signature is not readable; and

3) French-language letter addressed to Mr. Jalali dated June 26, 2001, an English translation of the letter, a Declaration, an Assignment, and a complete copy of the application Serial No. 09/581,272 and Preliminary Amendment that were enclosed with the letter.

The above letters and enclosures are additional documentary evidence that every effort has been made to obtain Mr. Jalali's signature on the application papers. None of the letters were returned as undeliverable and Mr. Hamon knows of no other means with which to contact Mr. Jalali.

Request for One-month Extension of Time

A one-month extension of time from August 25, 2002 to September 25, 2002 is requested in which to file the additional *Renewed Petition Under 37 CFR 1.47(a)* and *Declaration and Statement of Facts by Christian Hamon*. Mr. Hamon is on holiday until August 26, 2002 and cannot be contacted before that date. I was advised by my French associate on August 21, 2002 that Mr. Hamon is expected to sign and return the additional Declaration during the week of August 26, 2002, and that it will be sent to me by courier shortly thereafter. Mr. Hamon's Declaration will then be filed in the U.S. Patent Office.

The Commissioner is hereby authorized to charge payment of any fees required to Deposit Account No. 14-1080.

Respectfully submitted,

James E. Nilles

James E. Nilles
Registration 16,663

Date: August 26, 2002

Nilles & Nilles, S.C.
U.S. Bank Center, Suite 2000
777 East Wisconsin Avenue
Milwaukee, WI 53202
Telephone: 414-276-0977
Facsimile: 414-276-0982

ds G:\Data\CLIENT\136\147\PTO-Response-8.doc

LA POSTE
FRANCE

AVIS DE RÉCEPTION D'UN ENVOI INTERNATIONAL

CN 07

PRIORITAIRE / PAR AVION

Bureau de dépôt: CESSON - SEVIGNE Cesson Sévigné

Date de dépôt: 1° 01/02/2001

Destinataire de l'envoi (nom, adresse, pays de destination) :
Ali Jabli + Jalal Alley
AGF STREET
ZAFARANIEH VALIASRAV
TEHERAN 1193771 IRAN

A remplir par l'expéditeur

Timbre du bureau renvoyant l'avis

Services courrier

- envoi standard
- colis postal
- livre
- sac M
- PRIORITAIRE
- ÉCONOMIQUE
- [x] n° de l'envoi : 02755
- valeur déclarée montant :

Services financiers

- mandat n° :
- montant :

A compléter par le bureau de dépôt

Renvoyer à

Nom ou raison sociale: C. HAMON ETRED

Rue et n°: 4 Rue du Clos Courtel

Code postal et localité: 35572 Cesson Sévigné

FRANCE

A remplir par l'expéditeur

A compléter à destination*

L'envoi mentionné ci-dessus a été dûment :

- remis
- payé
- inscrit sur CCP

Date et signature:

* Cet Avis pourra être signé par le destinataire ou, si les règlements du pays de destination le prévoient, par une autre personne autorisée ou par l'agent du bureau de destination.

IMPRIMERIE NATIONALE

CN 07

LA POSTE

FRANCE 98

LA POSTE, OPÉRATEUR OFFICIEL DU COURRIER

AR

Serial No.: 09/581,272 Filing Date: 6/7/00

Inventor(s): Jalali et al.

Docket No.: 136.147 Atty.: JEN:ds Customer No. 23907

The following dated 4/11/02 has been received in the U.S. Patent Office on the DATE STAMPED hereon:

_____Amendment
_____Issue Fee Transmittal Letter
_____Letter
_____Letter to the Official Draftsperson
_____Drawings ________ Sheets
_____Check in the amount of $________ Check No. ________
_____Information Disclosure Statement, Form(s) PTO/SB/08A and PTO/SB/08B & cited references
_____Priority Document
x Other Renewed Petition Under 37 CFR 1.47(a); Declaration & Statement of Facts by Christian Hamon; original certified return receipt postcard

JC20 Rec'd PCT/PTO 23 APR 2002

PM APR MILWAUKEE WI APR 11 '02 8157263 U.S. POSTAGE

Nilles & Nilles, S.C.
Firstar Center, Suite 2000
777 East Wisconsin Avenue
Milwaukee, WI 53202

33

France Telecom

Development Division
France Telecom R&D

Your correspondent : C.HAMON
Phone : 02 99 12 48 05
Ref : FTRD/VAT/VPI/REN/009/01/CH

Mr ALI JALALI

At Mr HAMID ESMAILI's
7, JALAL ALLEY
ASEF STREET
ZAFARANIEH, VALIASRAV
TEHERAN (19877)

Cesson-Sévigné, February 1st, 2001

Subject : US Patent Application n° 09/581,272 filed December 7, 1998 concerning a "Method of calculating the Fast Fourier Transform and the Inverse Fast Fourier Transform", ref CCETT9498; US Declarations;

Sir,

Please find enclosed for signature a declaration concerning the US Patent Application you are one of the inventors. This signature is indispensable for us, the first and second mails being left unanswered.

Thank you for sending the executed document back us within three weeks.

We are looking forward to hearing from you.

The person in charge for Protection
in France Telecom R&D Rennes

C.HAMON

France Telecom R&D
4, rue du Clos-Courtel – BP 59 – 35512 Cesson-Sévigné Cedex
Phone : 02 99 12 41 11 Facsimile : 02 99 12 40 98
International : +33 2 12 41 44 Fax : +33 2 12 40 98

france telecom

Branche Développement
France Télécom R&D

Votre correspondant : C. Hamon
Téléphone : 02 99 12 48 05
Référence : FTRD/VAT/VPI/REN/009/01/CH

Monsieur ALI JALALI

Chez M. HAMID ESMAILI
7, JALAL ALLEY
ASEF STREET
ZAFARANIEH, VALIASRAV
TEHERAN (19877)

Cesson-Sévigné, le 1er février 2001

OBJET : Demande de brevet aux USA n° 09/581,272 du 07.12. concernant un « procédé de calcul de la transformée de Fourier rapide et de la transformée de Fourier rapide inverse», ref CCETT9498 ; Pouvoirs aux USA

Monsieur,

Je vous adresse ci-joint pour signature un pouvoir relatif à la demande de brevet US du brevet dont vous êtes un des inventeurs. Cette signature nous est indispensable, nos premier et deuxième envois étant restés sans réponse.

Merci de nous renvoyer ce document signé sous trois semaines.

Dans cette attente, nous vous prions d'agréer, Monsieur, l'expression de nos sentiments distingués.

Le Responsable Protection France Télécom R&D Rennes

Christian HAMON

France Télécom R&D
Site de Rennes
4, rue du Clos-Courtel - BP 59 - 35512 Cesson-Sévigné Cedex
Téléphone : 02 99 12 41 11 - Télécopie : 02 99 12 40 98
International : + 33 2 12 41 44 – Fax : +33 2 12 40 98

02966

DECLARATION FOR UTILITY OR DESIGN PATENT APPLICATION (37 CFR 1.63)

☐ Declaration Submitted with Initial Filing
OR
☑ Declaration Submitted after Initial Filing (surcharge (37 CFR 1.16(e)) required)

Attorney Docket Number: **136.147**
First Named Inventor: **Ali JALALI et al.**
COMPLETE IF KNOWN
Application Number: 09/581,272
Filing Date: June 7, 2000
Group Art Unit: ______________________
Examiner Name: ______________________

As a below named inventor, I hereby declare that:

My residence, post office address, and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:
Method of Calculating the Fast Fourier Transform and the Inverse Fast Fourier Transform

the specification of which
☐ is attached hereto
OR
☑ was filed on June 7, 2000 as United States Application No. 09/581,272 and was amended on June 7, 2000.

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to the patentability as defined in 37 CFR 1.56.

I hereby claim foreign priority benefits under 35 U.S.C. 119(a)–(d) or 365(b) of any foreign application(s) for patent or inventor's certificate, or 365(a) of any PCT international application which designated at least one country other than the United States of America, listed below and have also identified below, by checking the box, any foreign application for patent or inventor's certificate, or of any PCT international application having a filing date before that of the application on which priority is claimed.

Prior Foreign Application(s)

(Number)	(Country)	(Month/Day/Year Filed)	Priority Not Claimed	Certified Copy Attached?
97 15737	France	December 8, 1997	☐	☐ Yes ☑ No
			☐	☐ Yes ☐ No
			☐	☐ Yes ☐ No

☐ Additional foreign application numbers are listed on a supplemental priority data sheet PTO/SB/02B attached hereto:

I hereby claim the benefit under 35 U.S.C. 119(e) of any United States provisional application(s) listed below.

(Application Number)	(Month/Day/Year Filed)

☐ Additional provisional application numbers are listed on a supplemental priority data sheet PTO/SB/02B attached hereto.

12966

DECLARATION — Utility or Design Patent Application

Inventors: Ali JALALI, Pierre LeRAY, Dominique LACROIX
Serial No. 09/581,272

I hereby claim the benefit under 35 U.S.C. 120 of any United States application(s), or 365(c) of any PCT international application designating the United States of America, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States or PCT International application in the manner provided by the first paragraph of 35 U.S.C. 112, I acknowledge the duty to disclose information which is material to the patentability as defined in 37 CFR 1.56 which became available between the filing date of the prior application and the national or PCT International filing date of this application.

U.S. Parent Application or PCT Parent Application(s)

(Number)	(Month/Day/Year Filed)	(Patent Number (if applicable))
PCT/FR98/02636	December 7, 2000	

☐ Additional U.S. or PCT International application numbers are listed on a supplemental priority data sheet PTO/SB/02B attached hereto.

As a named inventor, I hereby appoint the following registered practitioner(s) to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith:

James E. Nilles, Reg. No. 16,663
Andrew J. Nilles, Reg. No. 31,786
Jay G. Durst, Reg. No. 41,723
Lisa M. Gehrke, Reg. No. 38,888
Thaddeus C. Stankowski, Reg. No. 45,522
Matthew C. Loppnow, Reg. No. 45,314
Stephen Michael Patton, Reg. No. 36,235
Jerome D. Drabiak, Reg. No. 31,011
Lisa A. Brzycki, Reg. No. 40,926

Direct all telephone calls to James E. Nilles at telephone number (414) 276-0977, facsimile number (414) 276-0982.
Direct all correspondence to: James E. Nilles
NILLES & NILLES, S.C.
Firstar Center, Suite 2000
777 East Wisconsin Avenue
Milwaukee, Wisconsin 53202-5345

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under 18 U.S.C. 1001 and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Full name of Sole or First Inventor: ☐ A petition has been filed for this unsigned inventor

Given Name (first & middle [if any]) & Family Name/Surname: Ali JALALI

Inventor's Signature: ALi JALALi Date:

Residence (city, state, country): Rennes, France
Citizenship: French

Post Office Address: 32 rue Mirabeau
Bâtiment K No. 234
(city, state, zip, country): 35700 Rennes, France

02966

DECLARATION – Utility or Design Patent Application

Inventors: Ali JALALI, Pierre LeRAY, Dominique LACROIX
Serial No. 09/581,272

Full name of Second Inventor, if any: ☐ A petition has been filed for this unsigned inventor

Given Name (first & middle [if any]) & Family Name/Surname: Pierre LERAY

Inventor's Signature: Pierre LERAY Date: 16/08/2000

Residence (city, state, country): Liffre, France Citizenship: French

Post Office Address: LaGrosse Roche

(city, state, zip, country): 35340 Liffre, France

Full name of Third Inventor, if any: ☐ A petition has been filed for this unsigned inventor

Given Name (first & middle [if any]) & Family Name/Surname: Dominique LACROIX

Inventor's Signature: Dominique LACROIX Date: 31/7/2000

Residence (city, state, country): Rennes, France Citizenship: French

Post Office Address: ~~18 Square Alain Fergent~~ 39 Square Louis Boulanger

(city, state, zip, country): ~~35000~~ 35700 Rennes, France

G:\Data\CLIENT\136\147\DECLARATION.doc

02966

ASSIGNMENT

WHEREAS, we, ALI JALALI, PIERRE LERAY, and DOMINIQUE LACROIX, have made a certain new and useful invention for which we are about to make application for Letters Patent of the United States, which application may be identified in the United States Patent Office as Serial No. 09/581,272, filed June 7, 2000, and entitled:

Method of Calculating the Fast Fourier Transform and the Inverse Fast Fourier Transform

(Executed: ________________)

WHEREAS, France Telecom SA, a company organized and existing under and by virtue of the laws of France and having its principal place of business at 6, Place d'Alleray, 75015 Paris, France and Telediffusion de France SA, a company organized and existing under and by virture of the laws of France and having its principal place of business at 10, rue d'Oradour-sur-Glane, 75732 Paris Cedex 15, France, are desirous of acquiring the entire interest in and to said invention, said application and the Letters Patent to be obtained therefor;

NOW, THEREFORE, for and in consideration of One Dollar and other good and valuable consideration to us in hand paid, the receipt and sufficiency whereof are hereby acknowledged, we have sold, assigned, and set over and by these presents do hereby sell, assign, and set over unto the said France Telecom SA and Telediffusion de France SA, said assignees' legal representatives, successors and assigns, the entire right, title and interest in and to any and all applications including the aforesaid application for Letters Patent heretofore and hereafter filed in the United States or any other country and which may be based in whole or in part on said inventions and discoveries, and in and to any and all Letters Patent heretofore or hereafter granted by the United States or any other country and which may be based in whole or in part on said inventions and discoveries, said assignment including the right to file and prosecute any and all such applications and also including the right to sue and recover for any and all infringements of said patents; and request the Commissioner of Patents to issue said Letters Patent to the above-mentioned assignee agreeably with the terms of this assignment.

THE UNDERSIGNED HEREBY GRANT the law firm of Nilles & Nilles, S.C. the power to insert in this instrument any further identification which may be necessary or desirable in order to comply with the rules of the United States Patent Office for recordation of this document.

UPON SAID CONSIDERATION, we do hereby covenant for ourselves and our heirs, legal representatives and assigns that neither we nor any of our said heirs, legal representatives or assigns have or will execute any instrument or perform any act in conflict herewith and that we or our said heirs, legal representatives and assigns will at all times do such acts and execute such papers, without expense to ourselves, as may be necessary or desirable in order to fully protect said inventions and discoveries for the benefit of said assignee, its successors or assigns, and to otherwise carry into full force and effect the text and interest of this assignment.

IN WITNESS WHEREOF, we have hereunto set our hands and affixed our seals.

Date: ____________ ____________________(SEAL)
Ali JALALIi

Date: 16/08/2000 ____________________(SEAL)
Pierre LERAY

Date: 31/7/2000 ____________________(SEAL)
Dominique LACROIX

NILLES & NILLES, S.C.

\\NTSERVER\COMPANY\Data\CLIENT\136\147\ASSIGNMENT.doc

france telecom

Monsieur Ali JALALI

chez M. HAMID ESMAÏLI

7 JALAL ALLEY

ASEF STREET

ZAFARANIEH, VALIASRAV

TEHERAN (19877) I.RAN

تهران - خیابان ولی‌عصر - زعفرانیه (نبش سرلشکر فلاحی)
نبش آصف - کوی جلال - پلاک ۷ منزل آقای حمید اسماعیلی کد پستی ۱۹۸۷۷
(از علی جلالی، برادر خط ...)

N'ôter pas les bandes blanches

LA POSTE
FRANCE

☐ DE RECEPTION D'UN ENVOI INTERNATIONAL
☐ AVIS DE PAIEMENT / INSCRIPTION D'UN MANDAT SUR UN CCP

CN 07
Service des Postes
PRIORITAIRE / PAR AVION

Bureau de dépôt	Date de dépôt
Cesson Sévigné	1er Fev 2001

Destinataire de l'envoi (nom, adresse, pays de destination) :
Ali Jabali 7 Jalal Alley
ASEF STREET
ZAFARANIEH, VALIASRAV
TEHERAN (19877) IRAN
A remplir par l'expéditeur

Timbre du bureau renvoyant l'avis

A compléter par le bureau de dépôt

Service courrier
☐ envoi standard ☐ colis postal ☐ livre ☐ sac M
☐ PRIORITAIRE ☐ ÉCONOMIQUE
☒ n° de l'envoi : ☐ valeur déclarée montant :

Services financiers
☐ mandat n° : montant :

A compléter à destination
L'envoi mentionné ci-dessus a été dûment :
☐ remis ☐ payé ☐ inscrit sur CCP
Date et signature*

Renvoyer à
A remplir par l'expéditeur
Nom ou raison sociale C. HAMON FTR&D
Rue et n° 4 Rue du Clos Courtel
Code postal et localité 35512 Cesson Sévigné
FRANCE

* Cet avis pourra être signé par le destinataire ou, si les règlements du pays de destination le prévoient, par une autre personne autorisée ou par l'agent du bureau de destination.

808 764 0 - 96/2862/7470/117/00
IMPRIMERIE NATIONALE 6 202033 O + D

Avis à coller par le guichetier

A POSTE

FICHE DE DÉPÔT D'UN ENVOI INTERNATIONAL

- RECOMMANDÉ
- AVEC VALEUR DÉCLARÉE

A remplir par l'expéditeur

stinataire : ALI JABALI 7 JALAL ALLEY
ASEF STREET
ZAFARANIEH, VALIASRAV
19877
Localité TEHERAN Pays IRAN

Réservé au service

date N°	Prix	vd	Nature	signature de l'agent
	Poids	crbt		

517/517 bis
VOIR AU DOS

RCS Nanterre B 356 000 000

LA POSTE

FRANCE

☐ AVIS DE RÉCEPTION D'UN ENVOI INTERNATIONAL

☒ ~~AVIS DE PAIEMENT / INSCRIPTION D'UN MANDAT SUR UN CCP~~

CN 07

Service des Postes

PRIORITAIRE / PAR AVION

Timbre du bureau renvoyant l'avis

Bureau de dépôt: CESSO[redacted] SEVIGNE

Ce[redacted] Sévigné

Date de dépôt: 1er 01/02/2001 [redacted]

Destinataire de l'envoi (nom, adresse, pays de destination) :

Ali Jabali 7 Jalal Alley

ASEF STREET

ZAFARANIEH, VALIASRAV

TEHERAN (19877) IRAN

A remplir par l'expéditeur

A compléter par le bureau de dépôt

Service courrier

☐ envoi standard ☐ colis postal ☐ livre ☐ sac M

☐ PRIORITAIRE ☐ ÉCONOMIQUE

ES PRIO

☒ n° de l'envoi : ☐ valeur déclarée montant :

02755

Services financiers

☐ mandat n° : montant :

A remplir par l'expéditeur

Renvoyer à

Nom ou raison sociale: C. HAMON FTRGD

Rue et n°: 4 Rue du Clos Courtel

Code postal et localité: 35512 Cesson Sévigné

FRANCE

A compléter à destination :

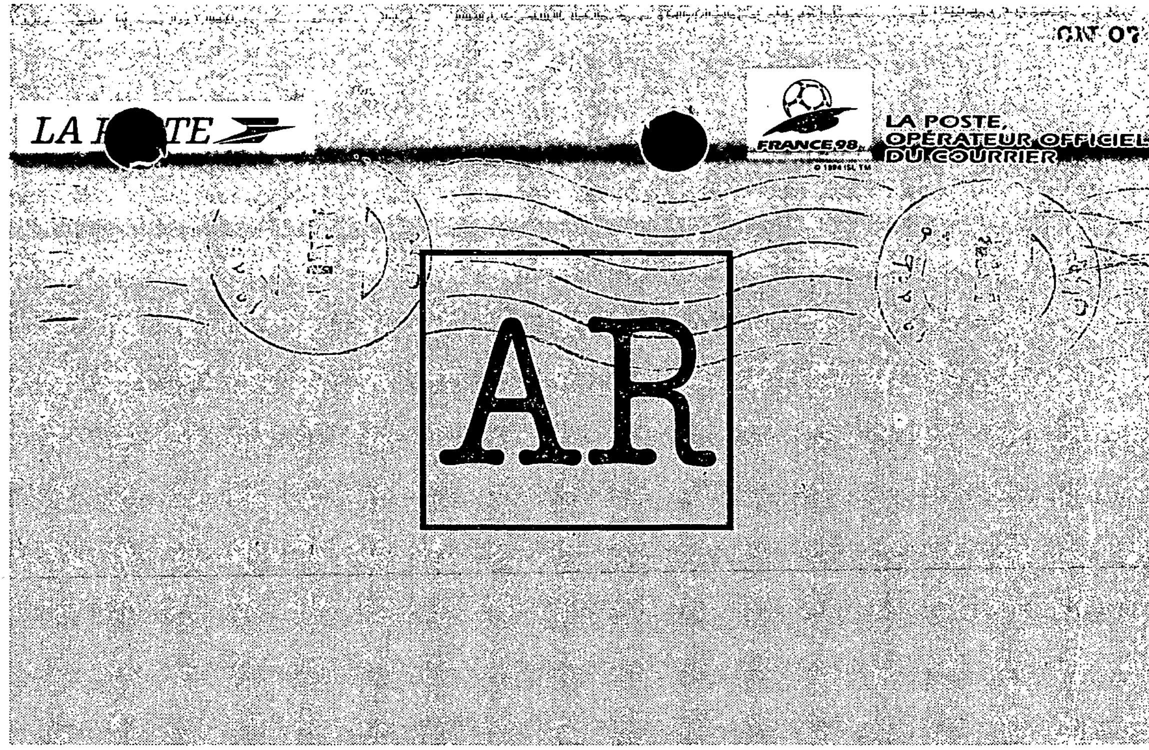
L'envoi mentionné ci-dessus a été dûment :

☐ remis ☐ payé ☐ inscrit sur CCP

Date et signature*

* Cet avis pourra être signé par le destinataire ou, si les règlements du pays de destination le prévoient, par une autre personne autorisée ou par l'agent du bureau de destination.

608 764 C - 96/2868/7470/117/OD
IMPRIMERIE NATIONALE 6 202033 G


LA POSTE, OPÉRATEUR OFFICIEL DU COURRIER
FRANCE 98
AR

France Telecom

Development Division
France Telecom R&D

Your correspondent : C.HAMON
Phone : 02 99 12 48 05
Ref : FTRD/DST/VPI/REN/113/00/CH

Mr ALI JALALI

At Mr HAMID ESMAILI's
7, JALAL ALLEY
ASEF STREET
ZAFARANIEH, VALIASRAV
TEHERAN (19877)

Cesson-Sévigné, September 28, 2000

Subject : US Patent Application n° 09/581,272 filed December 7, 1998 concerning a "Method of calculating the Fast Fourier Transform and the Inverse Fast Fourier Transform", ref CCETT9498; US Declarations;

Sir,

Please find enclosed for signature a declaration concerning the US Patent Application you are one of the inventors. This signature is indispensable for us, the first mail being left unanswered.

Thank you for sending the executed document back us within three weeks.

We are looking forward to hearing from you.

The person in charge for Protection
in France Telecom R&D Rennes

C.HAMON

France Telecom R&D
4, rue du Clos-Courtel – BP 59 – 35512 Cesson-Sévigné Cedex
Phone : 02 99 12 41 11 Facsimile : 02 99 12 40 98
International : +33 2 12 41 44 Fax : +33 2 12 40 98

france teleco

Branche Développeme
France Télécom R&D

4 OCT.

Votre correspondant : C. Hamon
Téléphone : 02 99 12 48 05
Référence : FTRD/DSTV/VPI/REN/113/00/CH

Monsieur ALI JALALI

Chez M. HAMID ESMAILI
7, JALAL ALLEY
ASEF STREET
ZAFARANIEH, VALIASRAV
TEHERAN (19877)

Cesson-Sévigné, le 28 septembre 2000

OBJET : Demande de brevet aux USA n° 09/581,272 du 07.12. concernant un « procédé de calcul de la transformée de Fourier rapide et de la trasnformée de Fourier rapide inverse», ref CCETT9498 ; Pouvoirs aux USA

Monsieur,

Je vous adresse ci-joint pour signature un pouvoir relatif à la demande de brevet US du brevet dont vous êtes un des inventeurs. Cette signature nous est indispensable, notre premier envoi étant resté sans réponse.

Merci de nous renvoyer ce document signé sous trois semaines.

Dans cette attente, nous vous prions d'agréer, Monsieur, l'expression de nos sentiments distingués.

Le Responsable Protection France Télécom R&D Rennes

Christian HAMON

France Télécom R&D
Site de Rennes
4, rue du Clos-Courtel - BP 59 - 35512 Cesson-Sévigné Cedex
Téléphone : 02 99 12 41 11 - Télécopie : 02 99 12 40 98
International : + 33 2 12 41 44 – Fax : +33 2 12 40 98

SA au capital de 4 098 458 244 EUR - RCS PARIS B 380 129 866

02966

DECLARATION FOR UTILITY OR DESIGN PATENT APPLICATION
(37 CFR 1.63)

☐ Declaration Submitted with Initial Filing
OR
☑ Declaration Submitted after Initial Filing (surcharge (37 CFR 1.16(e)) required)

Attorney Docket Number: **136.147**
First Named Inventor: **Ali JALALI et al.**
COMPLETE IF KNOWN
Application Number: 09/581,272
Filing Date: June 7, 2000
Group Art Unit: ____________________
Examiner Name: ____________________

As a below named inventor, I hereby declare that:

My residence, post office address, and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:
Method of Calculating the Fast Fourier Transform and the Inverse Fast Fourier Transform

the specification of which
☐ is attached hereto
OR
☑ was filed on June 7, 2000 as United States Application No. 09/581,272 and was amended on June 7, 2000.

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to the patentability as defined in 37 CFR 1.56.

I hereby claim foreign priority benefits under 35 U.S.C. 119(a)–(d) or 365(b) of any foreign application(s) for patent or inventor's certificate, or 365(a) of any PCT international application which designated at least one country other than the United States of America, listed below and have also identified below, by checking the box, any foreign application for patent or inventor's certificate, or of any PCT international application having a filing date before that of the application on which priority is claimed.

Prior Foreign Application(s)

(Number)	(Country)	(Month/Day/Year Filed)	Priority Not Claimed	Certified Copy Attached?
97 15737	France	December 8, 1997	☐	☐ Yes ☑ No
			☐	☐ Yes ☐ No
			☐	☐ Yes ☐ No

☐ Additional foreign application numbers are listed on a supplemental priority data sheet PTO/SB/02B attached hereto:

I hereby claim the benefit under 35 U.S.C. 119(e) of any United States provisional application(s) listed below.

(Application Number)	(Month/Day/Year Filed)

☐ Additional provisional application numbers are listed on a supplemental priority data sheet PTO/SB/02B attached hereto.

12966

DECLARATION FOR Utility or Design Patent Application

Inventors: Ali JALALI, Pierre LeRAY, Dominique LACROIX
Serial No. 09/581,272

I hereby claim the benefit under 35 U.S.C. 120 of any United States application(s), or 365(c) of any PCT international application designating the United States of America, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States or PCT international application in the manner provided by the first paragraph of 35 U.S.C. 112, I acknowledge the duty to disclose information which is material to the patentability as defined in 37 CFR 1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this application.

U.S. Parent Application or PCT Parent Application(s)

(Number)	(Month/Day/Year Filed)	(Patent Number (if applicable))
PCT/FR98/02636	December 7, 2000	

☐ Additional U.S. or PCT international application numbers are listed on a supplemental priority data sheet PTO/SB/02B attached hereto.

As a named inventor, I hereby appoint the following registered practitioner(s) to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith:

James E. Nilles, Reg. No. 16,663
Andrew J. Nilles, Reg. No. 31,786
Jay G. Durst, Reg. No. 41,723
Lisa M. Gehrke, Reg. No. 38,888
Thaddeus C. Stankowski, Reg. No. 45,522
Matthew C. Loppnow, Reg. No. 45,314
Stephen Michael Patton, Reg. No. 36,235
Jerome D. Drabiak, Reg. No. 31,011
Lisa A. Brzycki, Reg. No. 40,926

Direct all telephone calls to James E. Nilles at telephone number (414) 276-0977, facsimile number (414) 276-0982.
Direct all correspondence to: James E. Nilles
NILLES & NILLES, S.C.
Firstar Center, Suite 2000
777 East Wisconsin Avenue
Milwaukee, Wisconsin 53202-5345

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under 18 U.S.C. 1001 and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Full name of Sole or First Inventor: ☐ A petition has been filed for this unsigned inventor

Given Name (first & middle [if any]) & Family Name/Surname: Ali JALALI

Inventor's Signature: ALi JALALi X Date: X

Residence (city, state, country): Rennes, France Citizenship: French

Post Office Address: 32 rue Mirabeau
Bâtiment K No. 234
(city, state, zip, country): 35700 Rennes, France

02966

DECLARATIO Utility or Design Patent Applicati

Inventors: Ali JALALI, Pierre LeRAY, Dominique LACROIX
Serial No. 09/581,272

Full name of Second Inventor, if any: ☐ A petition has been filed for this unsigned inventor

Given Name (first & middle [if any]) & Family Name/Surname: Pierre LERAY

Inventor's Signature: Pierre LERAY Date: 16/08/2000

Residence (city, state, country): Liffre, France Citizenship: French

Post Office Address: LaGrosse Roche

(city, state, zip, country): 35340 Liffre, France

Full name of Third Inventor, if any: ☐ A petition has been filed for this unsigned inventor

Given Name (first & middle [if any]) & Family Name/Surname: Dominique LACROIX

Inventor's Signature: Dominique LACROIX Date: 31/7/2000

Residence (city, state, country): Rennes, France Citizenship: French

Post Office Address: ~~18 Square Alain Fergent~~ 39 Square Louis Boulanger

(city, state, zip, country): ~~35000~~ 35700 Rennes, France

G:\Data\CLIENT\136\147\DECLARATION.doc

02966

ASSIGNMENT

WHEREAS, we, ALI JALALI, PIERRE LERAY, and DOMINIQUE LACROIX, have made a certain new and useful invention for which we are about to make application for Letters Patent of the United States, which application may be identified in the United States Patent Office as Serial No. 09/581,272, filed June 7, 2000, and entitled:

Method of Calculating the Fast Fourier Transform and the Inverse Fast Fourier Transform

(Executed: ______________)

WHEREAS, France Telecom SA, a company organized and existing under and by virtue of the laws of France and having its principal place of business at 6, Place d'Alleray, 75015 Paris, France and Telediffusion de France SA, a company organized and existing under and by virture of the laws of France and having its principal place of business at 10, rue d'Oradour-sur-Glane, 75732 Paris Cedex 15, France, are desirous of acquiring the entire interest in and to said invention, said application and the Letters Patent to be obtained therefor;

NOW, THEREFORE, for and in consideration of One Dollar and other good and valuable consideration to us in hand paid, the receipt and sufficiency whereof are hereby acknowledged, we have sold, assigned, and set over and by these presents do hereby sell, assign, and set over unto the said France Telecom SA and Telediffusion de France SA, said assignees' legal representatives, successors and assigns, the entire right, title and interest in and to any and all applications including the aforesaid application for Letters Patent heretofore and hereafter filed in the United States or any other country and which may be based in whole or in part on said inventions and discoveries, and in and to any and all Letters Patent heretofore or hereafter granted by the United States or any other country and which may be based in whole or in part on said inventions and discoveries, said assignment including the right to file and prosecute any and all such applications and also including the right to sue and recover for any and all infringements of said patents; and request the Commissioner of Patents to issue said Letters Patent to the above-mentioned assignee agreeably with the terms of this assignment.

THE UNDERSIGNED HEREBY GRANT the law firm of Nilles & Nilles, S.C. the power to insert in this instrument any further identification which may be necessary or desirable in order to comply with the rules of the United States Patent Office for recordation of this document.

UPON SAID CONSIDERATION, we do hereby covenant for ourselves and our heirs, legal representatives and assigns that neither we nor any of our said heirs, legal representatives or assigns have or will execute any instrument or perform any act in conflict herewith and that we or our said heirs, legal representatives and assigns will at all times do such acts and execute such papers, without expense to ourselves, as may be necessary or desirable in order to fully protect said inventions and discoveries for the benefit of said assignee, its successors or assigns, and to otherwise carry into full force and effect the text and interest of this assignment.

IN WITNESS WHEREOF, we have hereunto set our hands and affixed our seals.

Date: ______________ ______________ (SEAL)
Ali JALALIi

Date: 16/08/2000 ______________ (SEAL)
Pierre LERAY

Date: 31/7/2000 ______________ (SEAL)
Dominique LACROIX

NILLES & NILLES, S.C.

\\NTSERVER\COMPANY\Data\CLIENT\136\147\ASSIGNMENT.doc

CABINET BALLOT-SCHMIT
Brevets - Marques - Dessins - Modèles - Contrats - Consultations - Litiges
4, rue Généra[redacted]che - Boîte Postale 855 - 56108 LO[redacted]T Cedex
Télép[redacted] 02 97 21 87 87 - Télécopie 02 97 64 5[redacted]
http://www.ballot-schmit.com - e.mail:info@ballot-schmit.com

COPIE

FRANCE TELECOM - CNET
DSTV/VPI - Service Gestion
38-40, rue du Général Leclerc
92794 ISSY MOULINEAUX CEDEX 9

A l'attention de M. Didier LEMOYNE

Lorient, le 10 août 2000

Copie : Mr HAMON (site Rennes).

Vos réf. : 9498/CNET2966
Dossier : 013770 US/pc
Lettre : BH/ml/00-0760.

Demande de brevet aux Etats-Unis n° **09/581 272**
Déposée : 07/12/1998
Titulaire : FRANCE TELECOM (SA) et TELEDIFFUSION DE FRANCE (SA)
Inventeur : JALALI Ali – LERAY Pierre – LACROIX Dominique.
Priorité : France – n° 97 15737 – 08/12/1997
Titre : PROCEDE DE CALCUL DE LA TRANSFORMEE DE FOURIER RAPIDE ET DE LA TRANSFORMEE DE FOURIER RAPIDE INVERSE.

Cher Monsieur,

Nous nous permettons de vous rappeler notre courrier du 05/07/2000 concernant le passage en phase nationale aux Etats-Unis de la demande internationale PCT/FR98/02636.

Comme nous vous le disions alors, afin de régulariser cette affaire, il est indispensable de déposer un pouvoir et une cession signés par les inventeurs, à savoir Madame Dominique LACROIX et Messieurs Ali JALALI et Pierre LERAY.

Ces documents ne nous étant toujours pas parvenus, le Patent Office américain vient de nous transmettre une notification fixant le délai de présentation desdits documents au 26 août prochain.

Nous vous demandons donc de nous les retourner **le plus rapidement possible** afin qu'ils puissent être déposés au Patent Office dans les délais impartis.

Dans l'attente de vous lire, nous vous prions d'agréer, Monsieur, l'expression de nos sentiments les meilleurs.

Bertrand HAYS

VEC VALEUR DÉCLARÉE

A remplir par l'expéditeur

Destinataire : Ali Jalali 7 Jalal Alley
ASEF STREET
ZAFARANEEH, VALIASRAV
19877

Localité TEHERAN Pays IRAN

Réservé au service

étiquette 510	date N°	Prix	vd	Nature	signature de l'agent
		Poids		crbt	

517/517 bis

VOIR AU DOS

RCS Nanterre B 356 000 000

N'ôtez pas les bandes blanches

Avis à coller par le guichetier

LA POSTE
FRANCE

☐ AVIS DE RÉCEPTION D'UN ENVOI INTERNATIONAL
☐ AVIS DE PAIEMENT / INSCRIPTION D'UN MANDAT SUR UN CCP

CN 07
Service des Postes
PRIORITAIRE / PAR AVION

Timbre du bureau renvoyant l'avis

A compléter par le bureau de dépôt

Bureau de dépôt: Cesson Sévigné
Date de dépôt:

Destinataire de l'envoi (nom, adresse, pays de destination) : Ali Jalali 7 Jalal Alley ASEF Street TEHERAN 19877 IRAN

A remplir par l'expéditeur

Service courrier
☐ envoi standard ☐ colis postal ☐ livre ☐ sac M
☐ PRIORITAIRE ☐ ÉCONOMIQUE
☒ n° de l'envoi : ☐ valeur déclarée montant :

Services financiers
☐ mandat n° : montant :

A compléter à destination

L'envoi mentionné ci-dessus a été dûment :
☐ remis ☐ payé ☐ inscrit sur CCP

Date et signature*

A remplir par l'expéditeur

Renvoyer à

Nom ou raison sociale C. HAMON. FTRetD
Rue et n° 4 Rue du clos Courtel
Code postal et localité 35512 Cesson Sévigné
FRANCE

* Cet avis pourra être signé par le destinataire ou, si les règlements du pays de destination le prévoient, par une autre personne autorisée ou par l'agent du bureau de destination.

6087640 - AVIS CN 07 - 970694747012P00 - D30

france telecom

Monsieur Ali JALALI

chez M. HAMID ESMAILI

7 JALAL ALLEY

ASEF STREET

ZAFARANIEH, VALIASRAV

TEHERAN (19877) I.RAN.

تهران - خیابان ولی‌عصر - زعفرانیه (خ سرلشکر فلاحی)

خ آصف - کوی جلال - پلاک ۷ منزل آقای حمید عرب اسماعیلی کد پ ۱۹۸۷۷

(ن، علی جلالی، برادر خانم)

France Telecom

Development Division
France Telecom R&D

Your correspondent : C. Hamon
Phone : 02 99 12 48 05
Ref : FTRD/VAT/PI/REN/062/01/CH

Mr ALI JALALI

At Mr HAMID ESMAILI's
7, JALAL ALLEY
ASEF STREET
ZAFARANIEH, VALIASRAV
TEHERAN (19877)

Cesson-Sévigné, June 26, 2001

Subject : US Patent Application n° 09/581,272 filed on December 7, 1998 concerning a "Method of calculating the Fast Fourier Transform and the Inverse Fast Fourier Transform", ref CCETT9498; US Declarations.

Sir,

Please find enclosed for signature a declaration concerning the US Patent Application you are one of the inventors. This signature is indispensable for us, the first and second and third mails being left unanswered.

We enclose a copy of the US patent application as well as the Preliminary Amendment.

Thank you for sending the executed document back us within three weeks.

We are looking forward to hearing from you.

The person in charge for Protection
in France Telecom R&D Rennes

Christian HAMON

France Telecom R&D
Office of Rennes
4, rue du Clos-Courtel – BP 59 – 35512 Cesson-Sévigné Cedex
Phone : 02 99 12 41 11 Facsimile : 02 99 12 40 98
International : +33 2 12 41 44 Fax : +33 2 12 40 98

Copie Mr HAYS le 26/06/01

france telecom

Branche Développement
France Télécom R&D

Votre correspondant : C. Hamon
Téléphone : 02 99 12 48 05
Référence : FTRD/VAT/PI/REN/062/01/CH

Monsieur ALI JALALI

Chez M. HAMID ESMAILI
7, JALAL ALLEY
ASEF STREET
ZAFARANIEH, VALIASRAV
TEHERAN (19877)

Cesson-Sévigné, le 26 juin 2001

OBJET : Demande de brevet aux USA n° 09/581,272 du 07.12. concernant un « procédé de calcul de la transformée de Fourier rapide et de la transformée de Fourier rapide inverse», ref CCETT9498 ; Pouvoirs aux USA

Monsieur,

Je vous adresse ci-joint pour signature un pouvoir relatif à la demande de brevet US du brevet dont vous êtes un des inventeurs. Cette signature nous est indispensable, nos premier et deuxième et troisième envois étant restés sans réponse.

Je vous joins une copie de la demande de brevet aux USA ainsi que l'amendement préliminaire .

Merci de nous renvoyer ce document signé sous trois semaines.

Dans cette attente, nous vous prions d'agréer, Monsieur, l'expression de nos sentiments distingués.

Le Responsable Protection France Télécom R&D Rennes

Christian HAMON

France Télécom R&D
Site de Rennes
4, rue du Clos-Courtel - BP 59 - 35512 Cesson-Sévigné Cedex
Téléphone : 02 99 12 41 11 - Télécopie : 02 99 12 40 98
International : + 33 2 12 41 44 – Fax : +33 2 12 40 98

LA POSTE
FRANCE

☐ AVIS DE RÉCEPTION D'UN ENVOI INTERNATIONAL
☐ AVIS DE PAIEMENT / INSCRIPTION D'UN MANDAT SUR UN CCP

CN 07
Service des Postes
PRIORITAIRE / PAR AVION

Timbre du bureau renvoyant l'avis

N'ôtez pas les bandes

Bureau de dépôt: Cesson Sé[illegible]é
Date de dépôt: 26 Juin 2001

Destinataire de l'envoi [illegible] (adresse, pays de destination) :
Ali Jalali 7 Jalal Alley
ASEF STREET
ZAFARANIEH, VALIASRAV
TEHERAN 19877 IRAN
A remplir par l'expéditeur

A compléter par le bureau de dépôt

Service courrier
☐ envoi standard ☐ colis postal ☐ livre ☐ sac M
☐ PRIORITAIRE ☐ ÉCONOMIQUE
☒ n° de l'envoi ☐ valeur déclarée
montant :

Services financiers
☐ mandat n° montant :

A compléter à destination
L'envoi mentionné ci-dessus a été dûment :
☐ remis ☐ payé ☐ inscrit sur CCP
Date et signature *

* Cet avis pourra être signé par le destinataire ou, si les règlements du pays de destination le prévoient, par une autre personne autorisée ou par l'agent du bureau de destination.

A remplir par l'expéditeur

Renvoyer à

Nom ou raison sociale

Rue et n°

Code postal et localité

FRANCE

608533 B - CN07 - V1 - 9923147470012300 - D17 - 902533

LA POSTE

FICHE DE DÉPÔT D'UN ENVOI INTERNATIONAL

- RECOMMANDÉ
- AVEC VALEUR DÉCLARÉE

A remplir par l'expéditeur

estinataire : ALI JALALI 7 JALAL ALLEY
ASET STREET
ZAFARANIEH, VALIASRAV
19877
Localité TEHERAN Pays IRAN

Réservé au service

date N°	Prix	vd	Nature	signature de l'agent
	Poids		crbt	

517/517 bis

VOIR AU DOS

RCS Nanterre B 356 000 000

02966

DECLARATION FOR UTILITY OR DESIGN PATENT APPLICATION
(37 CFR 1.63)

☐ Declaration Submitted with Initial Filing
OR
☑ Declaration Submitted after Initial Filing (surcharge (37 CFR 1.16(e)) required)

Attorney Docket Number: 136.147
First Named Inventor: Ali JALALI et al.
COMPLETE IF KNOWN
Application Number: 09/581,272
Filing Date: June 7, 2000
Group Art Unit:
Examiner Name:

As a below named inventor, I hereby declare that:

My residence, post office address, and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:
Method of Calculating the Fast Fourier Transform and the Inverse Fast Fourier Transform

the specification of which
☐ is attached hereto
OR
☑ was filed on June 7, 2000 as United States Application No. 09/581,272 and was amended on June 7, 2000.

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to the patentability as defined in 37 CFR 1.56.

I hereby claim foreign priority benefits under 35 U.S.C. 119(a)–(d) or 365(b) of any foreign application(s) for patent or inventor's certificate, or 365(a) of any PCT International application which designated at least one country other than the United States of America, listed below and have also identified below, by checking the box, any foreign application for patent or inventor's certificate, or of any PCT International application having a filing date before that of the application on which priority is claimed.

Prior Foreign Application(s)

(Number)	(Country)	(Month/Day/Year Filed)	Priority Not Claimed	Certified Copy Attached?
97 15737	France	December 8, 1997	☐	☐ Yes ☑ No
			☐	☐ Yes ☐ No
			☐	☐ Yes ☐ No

Additional foreign application numbers are listed on a supplemental priority data sheet PTO/SB/02B attached hereto:

I hereby claim the benefit under 35 U.S.C. 119(e) of any United States provisional application(s) listed below.

(Application Number)	(Month/Day/Year Filed)

☐ Additional provisional application numbers are listed on a supplemental priority data sheet PTO/SB/02B attached hereto.

12966

DECLARATION — ility or Design Patent Application

Inventors: Ali JALALI, Pierre LeRAY, Dominique LACROIX
Serial No. 09/581,272

I hereby claim the benefit under 35 U.S.C. 120 of any United States application(s), or 365(c) of any PCT international application designating the United States of America, listed below and, insofar as the subject matter of each of the claims of this application is ıot disclosed in the prior United States or PCT international application in the manner provided by the first paragraph of 35 U.S.C. 112, I acknowledge the duty to disclose information which is material to the patentability as defined in 37 CFR 1.56 which became ıvailable between the filing date of the prior application and the national or PCT international filing date of this application.

J.S. Parent Application or PCT Parent Application(s)

(Number)	(Month/Day/Year Filed)	(Patent Number (if applicable))
PCT/FR98/02636	December 7, 2000	

Additional U.S. or PCT international application numbers are listed on a supplemental priority data sheet PTO/SB/02B attached hereto.

: a named inventor, I hereby appoint the following registered practitioner(s) to prosecute this application and to transact all siness in the Patent and Trademark Office connected therewith:

James E. Nilles, Reg. No. 16,663
Andrew J. Nilles, Reg. No. 31,786
Jay G. Durst, Reg. No. 41,723
Lisa M. Gehrke, Reg. No. 38,888
Thaddeus C. Stankowski, Reg. No. 45,522
Matthew C. Loppnow, Reg. No. 45,314
Stephen Michael Patton, Reg. No. 36,235
Jerome D. Drablak, Reg. No. 31,011
Lisa A. Brzycki, Reg. No. 40,926

:ct all telephone calls to James E. Nilles at telephone number (414) 276-0977, facsimile number (414) 276-0982.
'ct all correspondence to: James E. Nilles
NILLES & NILLES, S.C.
Firstar Center, Suite 2000
777 East Wisconsin Avenue
Milwaukee, Wisconsin 53202-5345

eby declare that all statements made herein of my own knowledge are true and that all statements made on information and f are believed to be true; and further that these statements were made with the knowledge that willful false statements and the ꞩo made are punishable by fine or imprisonment, or both, under 18 U.S.C. 1001 and that such willful false statements may ırdize the validity of the application or any patent issued thereon.

name of Sole or First Inventor:

☐ A petition has been filed for this unsigned inventor

Name (first & middle [if any]) & Family Name/Surname: Ali JALALI

or's Signature: ALi JALALi ×

Date: ×

nce (city, state, country): Rennes, France

Post Office Address: 32 rue Mirabeau
Bâtiment K No. 234

(city, state, zip, country): 35700 Rennes, France

Citizenship: French

02966

DECLARATION — [illegible]ity or Design Patent Application

Inventors: Ali JALALI, Pierre LeRAY, Dominique LACROIX
Serial No. 09/581,272

Full name of Second Inventor, if any: ☐ A petition has been filed for this unsigned inventor

Given Name (first & middle [if any]) & Family Name/Surname: Pierre LERAY

Inventor's Signature: Pierre LERAY Date: 16/08/2000

Residence (city, state, country): Liffre, France Citizenship: French

Post Office Address: LaGrosse Roche

(city, state, zip, country): 35340 Liffre, France

Full name of Third Inventor, if any: ☐ A petition has been filed for this unsigned inventor

Given Name (first & middle [if any]) & Family Name/Surname: Dominique LACROIX

Inventor's Signature: Dominique LACROIX Date: 31/7/2000

Residence (city, state, country): Rennes, France Citizenship: French

Post Office Address: ~~18 Square Alain Fergent~~ 39 Square Louis Boulanger

(city, state, zip, country): ~~35000~~ 35700 Rennes, France

Data\CLIENT\136\147\DECLARATION.doc

02966

ASSIGNMENT

WHEREAS, we, ALI JALALI, PIERR[redacted]RAY, and DOMINIQUE LACROIX, [redacted]ave made a certain new and useful invention for which we are about to make application for Letters Patent of the United States, which application may be identified in the United States Patent Office as Serial No. 09/581,272, filed June 7, 2000, and entitled:

Method of Calculating the Fast Fourier Transform and the Inverse Fast Fourier Transform

(Executed: ______________)

WHEREAS, France Telecom SA, a company organized and existing under and by virtue of the laws of France and having its principal place of business at 6, Place d'Alleray, 75015 Paris, France and Telediffusion de France SA, a company organized and existing under and by virture of the laws of France and having its principal place of business at 10, rue d'Oradour-sur-Glane, 75732 Paris Cedex 15, France, are desirous of acquiring the entire interest in and to said invention, said application and the Letters Patent to be obtained therefor;

NOW, THEREFORE, for and in consideration of One Dollar and other good and valuable consideration to us in hand paid, the receipt and sufficiency whereof are hereby acknowledged, we have sold, assigned, and set over and by these presents do hereby sell, assign, and set over unto the said France Telecom SA ·d Telediffusion de France SA, said assignees' legal representatives, successors and assigns, the entire right, title and interest in and to any and all applications including the aforesaid application for Letters Patent heretofore and hereafter filed in the United States or any other country and which may be based in whole or in part on said inventions and discoveries, and in and to any and all Letters Patent heretofore or ıereafter granted by the United States or any other country and which may be based in whole or in part ›n said inventions and discoveries, said assignment including the right to file and prosecute any and all uch applications and also including the right to sue and recover for any and all infringements of said atents; and request the Commissioner of Patents to issue said Letters Patent to the above-mentioned ssignee agreeably with the terms of this assignment.

HE UNDERSIGNED HEREBY GRANT the law firm of Nilles & Nilles, S.C. the power to insert in is instrument any further identification which may be necessary or desirable in order to comply with the les of the United States Patent Office for recordation of this document.

PON SAID CONSIDERATION, we do hereby covenant for ourselves and our heirs, legal presentatives and assigns that neither we nor any of our said heirs, legal representatives or assigns have will execute any instrument or perform any act in conflict herewith and that we or our said heirs, legal presentatives and assigns will at all times do such acts and execute such papers, without expense to lves, as may be necessary or desirable in order to fully protect said inventions and discoveries for the nefit of said assignee, its successors or assigns, and to otherwise carry into full force and effect the text d interest of this assignment.

WITNESS WHEREOF, we have hereunto set our hands and affixed our seals.

te: ______________ | ______________ (SEAL) Ali JALALIi

te: 16/08/2000 | ______________ (SEAL) Pierre LERAY

e: 31/7/2000 | ______________ (SEAL) Dominique LACROIX

LES & NILLES, S.C.

\\NTSERVER\COMPANY\Data\CLIENT\136\147\ASSIGNMENT.doc

Monsieur Ali JALALi

chez M. HAMID ESMAïLi

7 JALAL ALLEY

ASEF STREET

ZAFARANIEH, VALIASRAV

TEHERAN (19877) I.RAN

تهران - خیابان ولی عصر - زعفرانیه (نبش خ سرلشکر فلاحی)

خیابان آصف - کوی جلال - پلاک ۷ - منزل آقای حمید عرب اسماعیلی - کد پستی ۱۹۸۷۷

(ن. علی جلالی، ساخط زنامه)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

National Phase of PCT/FR98/02636

International Filing Date: 07 December 1998

Inventors: Ali JALALI, Pierre LERAY, and Dominique LACROIX

Title: METHOD OF CALCULATING THE FAST FOURIER TRANSFORM AND THE INVERSE FAST FOURIER TRANSFORM

Priority: French Application No. 97 15737
Filed 08 December 1997

Attorney Docket 136.147

PRELIMINARY AMENDMENT

DO/EO/US
Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

This Preliminary Amendment is directed to a new U.S. application as identified above. Please enter this preliminary amendment prior to calculating the fees.

Please substitute the attached claims 1-42, which incorporate amendments made to claim 2 during international preliminary examination, for pages 36-51 containing claims 1-42. Please use the substitute claims for examination purposes.

Please amend the specification, substitute claims, and Abstract as follows:

IN THE SPECIFICATION

Page 1, after the title insert the heading -- BACKGROUND OF THE INVENTION --; and the subheading -- 1. Field of the Invention --;

between lines 5 and 6, insert the subheading

-- 2. Description of the Related Art --;

Page 7, between lines 28 and 29, insert the heading -- OBJECTS AND SUMMARY

: THE INVENTION --;

Page 10, between lines 26 and 27, insert the heading -- BRIEF DESCRIPTION OF

HE DRAWINGS --;

Page 13, between lines 8 and 9, insert the heading -- DESCRIPTION OF THE

REFERRED EMBODIMENTS --.

<u>IN THE CLAIMS, As Amended Under Article 34</u>

Claim 3, lines 1 and 2, cancel "or 2";

Claim 5, lines 2 and 3, cancel "in turn dependent on claim 3, in turn dependent on

:laim 1,";

Claim 6, lines 1, 2 and 3, cancel "in turn dependent on claim 3, in turn dependent on

:laim 1, or according to claim 5,"

Claim 23, lines 1 and 2, cancel "or 20";

Claim 25, line 2, cancel "or 24";

Claim 26, line 2, cancel "or 25";

Claim 29, lines 2 and 3, cancel "in turn dependent on claim 3, in turn dependent on

claim 2,"

Claim 31, line 2, cancel "or 30";

Claim 35, line 2, cancel "or 34";

Claim 39, line 2, cancel "or 38";

Claim 40, line 2, cancel "or 39".

IN THE ABSTRACT

Please substitute the attached *Abstract of the Disclosure* for the Abstract as filed.

REMARKS

This application has been amended to incorporate the modifications made to the international application under Article 34 which include changes made to claim 2. The application is further amended to insert headings in the specification, eliminate the multiple claim dependencies, and to conform the Abstract in accordance with U.S. Patent Office practice. Entry of the amendments and early consideration and allowance are respectfully requested.

Respectfully submitted,

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METHOD OF CALCULATING THE FAST FOURIER TRANSFORM AND THE INVERSE FAST FOURIER TRANSFORM

Abstract of the Disclosure

A method of calculating the fast Fourier transform or the inverse fast Fourier transform of a series of N real samples x(n), with N power of two, operating according to a time interleaving algorithm and providing the sample series X(n) in ascending order to index n and using limited calculating and storage means. A method of calculating the fast Fourier transform or the inverse fast Fourier transform of a series of N conjugated complex samples X(n), with N power of two, operating according to a frequency interleaving algorithm.

AMENDED CLAIMS

1. A method of calculating the fast Fourier transform or the inverse fast Fourier transform of a digital signal defined by a series of N real starting samples x(n), with N a power of two and n ∈ [0..N-1], comprising successive transformation steps (2) for transforming input samples into output samples, all the transformation steps being performed by means of a single set of butterflies with several inputs and several outputs, the operating mode of which is modified selectively in each transformation step, the input and output samples of each transformation step being stored in a storage memory, a series of N output samples y(n) representative of the fast Fourier transform or the inverse fast Fourier transform of the output samples x(n) being provided in the last transformation step,

characterized in that output samples y(n) are real,

and in that the output samples of a butterfly replace the corresponding input samples of the same rank in the storage memory, so that, if the starting samples x(n) processed in the first transformation step are classified in bit-reversed order of their index n, output samples y(n) are provided in the last transformation step in ascending order of index n, these output samples being defined by the following relations:

y(0) = Re[X(0)]

$y(n) = Re[X((n+1)/2)]$ for n being odd and different from N-1

$y(n) = Im[X(n/2)]$ for n being even and different from 0

$y(N-1) = Re[X(N/2)]$

where samples X(n), with $n \in [0..N-1]$, designate the complex samples of the series corresponding to the fast or inverse fast Fourier transform of the starting sample series x(n).

2. A method of calculating the fast Fourier transform or the inverse fast Fourier transform of a digital signal defined by a series of N complex samples X(n) conjugated by pairs, characterized in that the calculation is done on a series of N real starting samples y(n) representative of the series of complex samples X(n), with N power of two and $n \in [0..N-1]$, the starting samples y(n) being defined as follows:

$y(0) = Re[X(0)]$

$y(n) = Re[X((n+1)/2)]$ for n being odd and different from N-1

$y(n) = Im[X(n/2)]$ for n being even and different from 0

$y(N-1) = Re[X(N/2)]$

in that this method comprises successive transformation steps for transforming input samples into output samples, a series of N real output samples x(n) representative of this fast or inverse fast Fourier transform being provided in the last transformation step, all the transformation steps being performed by means of a single set of butterflies with several inputs and several outputs, the operating mode of which

is modified selectively in each transformation step, the input and output samples of each transformation step being stored in a storage memory,

and in that the output samples of a butterfly replace the corresponding input samples of the same rank in the storage memory, so that, if the starting samples $y(n)$ processed in the first transformation step are classified in ascending order of index n, the output samples $x(n)$ are provided in the last transformation step in bit-reversed order of index n.

3. The calculation method according to claim 1 or 2, characterized in that, in each transformation step, each butterfly transforms input sample pairs, the ranks of the input samples of the same pair within the series of input samples of said transformation step being symmetrical with respect to a center between the end rank values of the input samples transformed by said butterfly.

4. The calculation method according to claim 3, characterized in that it comprises μ-1 transformation steps E_p with $\mu=\log_2(N)$ and $p \in [0..\mu-2]$.

5. The calculation method according to claim 4, in turn dependent on claim 3, in turn dependent on claim 1, characterized in further comprising:

- a preliminary step of modifying the sequence of the starting samples $x(n)$ ranked in ascending order of index n and showing them in bit-reversed order of index n in the first transformation step, and

- a final step of processing the series of output samples $y(n)$ and providing a series of N complex

conjugated samples X(n) corresponding to the fast or the inverse fast Fourier transform of the series of starting samples x(n).

6. The calculation method of claim 4, in turn dependent on claim 3, in turn dependent on claim 1, or according to claim 5, characterized in that, in each transformation step E_p, butterflies are distributed among $N/2^{p+2}$ calculation blocks,

in that each calculation block has a peripheral butterfly and/or 2^p-1 internal butterflies,

in that the peripheral butterfly of the rank α calculation block in transformation step E_β transforms the input samples of rank $2^{\beta+2}\alpha$, $2^{\beta+2}\alpha+2^{\beta+1}-1$, $2^{\beta+2}\alpha+2^{\beta+1}$, $2^{\beta+2}\alpha+2^{\beta+2}-1$ into output samples of the same rank,

and in that the internal rank τ butterfly of the rank α calculation block in transformation step E_β transforms the input samples of rank $2^{\beta+2}\alpha+2\tau+1$, $2^{\beta+2}\alpha+2\tau+2$, $2^{\beta+2}\alpha+2^{\beta+2}-2\tau-3$, $2^{\beta+2}\alpha+2^{\beta+2}-2\tau-2$ into output samples of the same rank, with $\beta\geq1$.

7. The calculation method according to claim 6, characterized in that each butterfly is assigned a coefficient W^s, whereon the calculation inside the butterfly is based, said coefficient being equal to $e^{-j(2\pi s/N)}$ with $s \in [0..N/4-1]$ for a fast Fourier transform and is equal to $e^{j(2\pi s/N)}$ with $s \in [0..N/4-1]$ for an inverse fast Fourier transform.

8. Calculation method according to claim 7, characterized in that the internal rank τ butterfly of the rank α calculation block in transformation step E_β is assigned coefficient W^δ with $\delta = (\tau+1).(N/2^{\beta+2})$.

9. The calculation method according to claim 8, characterized in that the butterflies for implementing the transformation steps are all of the same type and have

- four inputs for receiving input samples and four outputs for providing output samples,

- four additional inputs, respectively primary mode, secondary mode, permutation, and coefficient inputs,

in order to selectively apply different transformation operations to the input samples, each operation being determined by the values assigned to the primary mode, secondary mode, permutation signals, and a coefficient admitted on said corresponding additional inputs.

10. The calculation method according to claim 9, characterized in that, for each butterfly, the primary mode signal is 0 for a peripheral butterfly and 1 for an internal butterfly,

in that the permutation signal is 0 for the even rank calculation blocks, including rank 0, and 1 for the other ones.

11. The calculation method according to claim 10, characterized in that, in transformation step Ep, each calculation block comprises one peripheral butterfly and 2^p-1 internal butterflies.

12. The calculation method according to claim 11, characterized in that the secondary mode signal is 1 if the peripheral butterfly is used for the last transformation step, and otherwise 0.

13. The calculation method according to claim 12, characterized in that, for four input samples e1, e2, e3, and e4, and for a complex coefficient $W^s=A+j.B$, the butterfly delivers the following output samples s1, s2, s3, and s4

1) if the primary mode and secondary mode signals are 0: s1 = e1 + e2

s2 = e1 − e2

s3 = e4 − e3

s4 = e3 + e4

2) if the primary mode signal is 0 and the secondary mode signal is 1:

s1 = e1 + e2 + e3 + e4

s2 = e1 − e2

s3 = e4 − e3

s4 = (e1 + e2) − (e3 + e4)

3) if the primary mode signal is 1 and the permutation signal is 0:

s1 = e1 + A.e3 − B.e4

s2 = e2 + B.e3 + A.e4

s3 = e1 − A.e3 + B.e4

s4 = −e2 + B.e3 + A.e4

4) if the primary mode signal is 1 and the permutation signal is 1:

s1 = e1 − A.e3 + B.e4

s2 = −e2 + B.e3 + A.e4

s3 = e1 − A.e3 − B.e4

s4 = e2 + B.e3 + A.e4

14. The calculation method according to claim 10, characterized in that, in transformation step E_p, each calculation block comprises:

- 2^p-1 internal butterflies and a peripheral butterfly for the even values of index p as well as for the last transformation step if μ is even, and
- 2^p-1 internal butterflies, otherwise.

15. The calculation method according to claim 13, characterized in that the secondary mode signal is 1 if the peripheral butterfly is used for the last transformation step with μ being odd, and otherwise 0.

16. The calculation method according to claim 15, characterized in that, for four input samples e1, e2, e3, and e4, and for a complex coefficient W^s=A+j.B, the butterfly delivers the following output samples s1, s2, s3, and s4

1) if primary mode, secondary mode and permutation signals are 0:

s1 = e1 + e2 + e3 + e4
s2 = e1 - e2
s3 = e4 - e3
s4 = (e1 + e2) - (e3 + e4)

2) if the primary mode signal is 0 and the secondary mode signal is 1:

s1 = e1 + e4
s2 = e2
s3 = e3
s4 = e1 - e4

3) if the primary mode signal is 0 and the permutation signal is 1:

s1 = (e3 + e4) - (e1 + e2)
s2 = e1 - e2
s3 = e4 - e3
s4 = e1 + e2 + e3 + e4

4) if the primary mode signal is 1 and the permutation signal is 0:

$$s1 = e1 + A.e3 - B.e4$$
$$s2 = e2 + B.e3 + A.e4$$
$$s3 = e1 - A.e3 + B.e4$$
$$s4 = -e2 + B.e3 + A.e4$$

5) if the primary mode signal is 1 and the permutation signal is 1:

$$s1 = e1 - A.e3 + B.e4$$
$$s2 = -e2 + B.e3 + A.e4$$
$$s3 = e1 + A.e3 - B.e4$$
$$s4 = e2 + B.e3 + A.e4$$

17. The calculation method according to claim 10, characterized in that, in transformation step E_p, each calculation block comprises:

- 2^p-1 internal butterflies and a peripheral butterfly for the even values of index p, and
- 2^p-1 internal butterflies, otherwise.

18. The calculation method according to claim 17, characterized in that the secondary mode signal is 1 if the peripheral butterfly is used for the first transformation step with μ being even, and otherwise 0.

19. The calculation method according to claim 18, characterized in that, for four input samples e1, e2, e3, and e4, and for a complex coefficient $W^s=A+j.B$, the butterfly delivers the following output samples s1, s2, s3, and s4

1) if the primary mode signal is 0 and the secondary mode signal is 1:

$$s1 = e1 + e2$$
$$s2 = e1 - e2$$

s3 = e4 - e3

s4 = e3 + e4

2) if primary mode, secondary mode and permutation signals are 0:

s1 = e1 + e2 + e3 + e4

s2 = e1 - e2

s3 = e4 - e3

s4 = (e1 + e2) - (e3 + e4)

3) if the primary mode and secondary mode signals are 0 and the permutation signal is 1:

s1 = (e3 + e4) - (e1 + e2)

s2 = e1 - e2

s3 = e4 - e3

s4 = e1 + e2 + e3 + e4

4) if the primary mode signal is 1 and the permutation signal is 0:

s1 = e1 + A.e3 - B.e4

s2 = e2 + B.e3 + A.e4

s3 = e1 - A.e3 + B.e4

s4 = -e2 + B.e3 + A.e4

5) if the primary mode signal is 1 and the permutation signal is 1:

s1 = e1 - A.e3 + B.e4

s2 = -e2 + B.e3 + A.e4

s3 = e1 + A.e3 - B.e4

s4 = e2 + B.e3 + A.e4

20. The calculation method according to claim 8, characterized in that the butterflies for implementing the transformation steps are all of the same type and have

- four inputs for receiving input samples and four outputs for providing output samples,

- four additional inputs, respectively primary mode, secondary mode, permutation, and coefficient inputs,

in order to selectively apply different transformation operations to the input samples, each operation being determined by the values assigned to the primary mode, secondary mode, permutation signals, and a coefficient admitted on said corresponding additional inputs,

and in that the final step furthermore performs an addition and subtraction between the first and the last output sample provided in the last transformation step.

21. The calculation method according to claim 20, characterized in that, in transformation step E_p, each calculation block comprises one peripheral butterfly and 2^p-1 internal butterflies.

22. The calculation method according to claim 21, characterized in that, for four input samples e1, e2, e3, and e4, and for a complex coefficient $W^s=A+j.B$, the butterfly delivers the following output samples s1, s2, s3, and s4

1) if the primary mode signal is 0:

s1 = e1 + e2

s2 = e1 - e2

s3 = e4 - e3

s4 = e3 + e4

2) if the primary mode signal is 1 and the permutation signal is 0:

s1 = e1 + A.e3 - B.e4

s2 = e2 + B.e3 + A.e4

s3 = e1 − A.e3 + B.e4

s4 = −e2 + B.e3 + A.e4

3) if the primary signal is 1 and the permutation signal is 1:

s1 = e1 − A.e3 + B.e4

s2 = −e2 + B.e3 + A.e4

s3 = e1 + A.e3 −B.e4

s4 = e2 + B.e3 + A.e4

23. The calculation method according to claim 9 or 20, characterized in that the first and second binary addresses of μ bits are generated for each butterfly, each binary address corresponding to the rank of an input sample of said butterfly and the second binary address being greater than the first binary address.

24. The calculation method according to claim 23, characterized in that said first and second binary addresses are consecutive and an internal butterfly is involved.

25. The calculation method according to claim 23 or 24, characterized in that, if a peripheral butterfly is involved, the p+2 low-order bits of the first address are equal to 0, and the p+2 low-order bits of the second address form a number equal to $2^{p+1}-1$.

26. The calculation method according to claim 24 or 25, characterized in that the address of the two other samples to be applied to the inputs of the butterfly, be they peripheral or internal, are obtained by inverting the (p+2) low-order bits of said first and second produced addresses.

27. The calculation method according to claim 26, characterized in that even-numbered address samples and odd-numbered address samples are stored in two separate memories.

28. The calculation method according to claim 25, characterized in that the value of the parameter s of the coefficient W^s assigned to an internal butterfly in transformation step E_p is coded by μ-2 bits, and is:

- if $p+1=\mu-2$, the number formed by the p+1 low-order bits of the second binary address produced for said internal butterfly,

- if $p+1<\mu-2$, the number formed by the p+1 low-order bits of the second binary address produced for said internal butterfly, followed by μ-p-3 zero bits at the end of the number,

- if $p+1>\mu-2$, the number formed by the p+1 low-order bits of the second binary address produced for said internal butterfly, minus its μ-p-1 low-order bits.

29. The calculation method according to claim 4, in turn dependent on claim 3, in turn dependent on claim 2, characterized in that in each transformation step E_p, the butterflies are distributed among 2^p calculation blocks,

in that each calculation block comprises one peripheral butterfly and $N/2^{p+2}-1$ internal butterflies,

in that the peripheral butterfly of the rank α calculation block in transformation step E_β transforms the input samples of rank $2^{\mu-\beta}\alpha$, $2^{\mu-\beta}\alpha+2^{\mu-\beta-1}-1$, $2^{\mu-\beta}\alpha+2^{\mu-\beta-1}$, $2^{\mu-\beta}\alpha+2^{\mu-\beta}-1$ into output samples of the same rank,

and in that the internal rank τ butterfly of the rank α calculation block in transformation step E_β transforms the input samples of rank $2^{\mu-\beta}\alpha+2\tau+1$, $2^{\mu-\beta}\alpha+2\tau+2$, $2^{\mu-\beta}\alpha+2^{\mu-\beta-1}-2\tau-3$, $2^{\mu-\beta}\alpha+2^{\mu-\beta}-2\tau-2$ into output samples of the same rank.

30. The calculation method according to claim 29, characterized in further comprising a final step of modifying the sequence of the output samples provided in the last transformation step and classifying them in ascending order of index n.

31. The calculation method according to claim 29 or 30, characterized in that each butterfly is assigned a coefficient W^s, whereon the calculation inside the butterfly is based, said coefficient being equal to $e^{-j(2\pi s/N)}$ with $s \in [0..N/4-1]$ for a fast Fourier transform and is equal to $e^{j(2\pi s/N)}$ with $s \in [0..N/4-1]$ for an inverse fast Fourier transform.

32. Calculation method according to claim 31, characterized in that the internal rank τ butterfly of the rank α calculation block in transformation step E_β is assigned coefficient W^δ with $\delta = (\tau+1).2^\beta$.

33. The calculation method according to claim 32, characterized in that the butterflies for implementing the transformation steps are all of the same type and have

- four inputs for receiving input samples and four outputs for providing output samples,

- four additional inputs, respectively primary mode, secondary mode, permutation, and coefficient inputs,

in order to selectively apply different transformation operations to the input samples, each operation being determined by the values assigned to the primary mode, secondary mode, permutation signals, and a coefficient admitted on said corresponding additional inputs.

34. The calculation method according to claim 33, characterized in that, for each butterfly, the primary mode signal is 0 for a peripheral butterfly and 1 for an internal butterfly,

in that the permutation signal is 0 for the even rank calculation blocks, including rank 0, and 1 for the odd values.

35. The calculation method according to claim 31 or 34, characterized in that the secondary mode signal is 1 if the butterfly, be it peripheral or internal, is used for the first transformation step, and otherwise 0.

36. The calculation method according to claim 35, characterized in that, for four input samples e1, e2, e3, and e4, and for a complex coefficient W^s=A+j.B, the butterfly delivers the following output samples s1, s2, s3, and s4

1) if the primary mode and secondary mode signals are 0:

$$s1 = (e1 + e2)/2$$
$$s2 = (e1 - e2)/2$$
$$s3 = (e4 - e3)/2$$
$$s4 = (e3 + e4)/2$$

2) if the primary mode signal is 0 and the secondary mode signal is 1:

s1 = [(e1+e4)/2-e2]/2

s2 = [(e1+e4)/2-e2]/2

s3 = [e3-(e1-e4)/2]/2

s4 = [e3+(e1+e4)/2]/2

3) if the primary mode signal is 1 and the permutation signal is 0:

s1 = (e1+e3)/2

s2 = (e2+e4)/2

s3 = [(e1-e3).A - (e2+e4).B]/2

s4 = [-(e1-e3).B + (e2+e4).A]/2

4) if the primary mode signal is 1 and the permutation signal is 1:

s1 = [(e1-e3).A - (e2+e4).B]/2

s2 = [-(e1-e3).B + (e2+e4).A]/2

s3 = (e1+e3)/2

s4 = (e2-e4)/2

37. The calculation method according to claim 33, characterized in that the first and second binary addresses of μ bits are generated for each butterfly, each binary address corresponding to the rank of an input sample of said butterfly and the second binary address being greater than the first binary address.

38. The calculation method according to claim 37, characterized in that said first and second binary addresses are consecutive and an internal butterfly is involved.

39. The calculation method according to claim 37 or 38, characterized in that, if a peripheral butterfly is involved, the μ-p low-order bits of the first address are equal to 0, and the μ-p low-order bits of the second address form a number equal to $N/2^{p+1}-1$.

40. The calculation method according to claim 38 or 39, characterized in that the address of the two other samples to be applied to the inputs of the butterfly are obtained by inverting the μ-p low-order bits of both produced addresses.

41. The calculation method according to claim 40, characterized in that even-numbered address samples and odd-numbered address samples are stored in two separate memories.

42. The calculation method according to claim 41, characterized in that the value of the parameter s of the coefficient W^s assigned to an internal butterfly in transformation step E_p is coded by μ-2 bits, and is:

- if $\mu-p-1=\mu-2$, the number formed by the μ-p-1 low-order bits of the second address produced for said internal butterfly,

- if $\mu-p-1<\mu-2$, the number formed by the μ-p-1 low-order bits of the second address produced for said internal butterfly, followed by p-1 zero bits at the end of the number,

- if $\mu-p-1>\mu-2$, the number formed by the μ-p-1 low-order bits of the second address produced for said internal butterfly, minus its p+1 low-order bits.

METHOD OF CALCULATING THE FAST FOURIER TRANSFORM AND THE INVERSE FAST FOURIER TRANSFORM

This invention relates to a method of calculating the fast Fourier transform or the inverse fast Fourier transform of a series of real numbers or a series of conjugated complex samples.

The Fourier transform is probably one of the most important tools for analyzing, designing and implementing signal processing algorithms, and the existence of efficient algorithms, such as that of the fast Fourier transform, has been a major factor for this situation. Although most Fourier transform algorithms are designed for transforming series of complex numbers, there are, however, various applications, such as image or acoustic signal processing or certain types of multicarrier modulation wherein the series to be transformed are real numbers.

In general, the direct Fourier transform and the inverse Fourier transform respectively set up the following relations between two series of N complex numbers, x(n) and X(n):

$$X(n) = \sum_{k=0}^{N-1} x(k)w^{kn} \quad \text{with } n \in [0 \dots N-1] \text{ and } w^{kn} = e^{-j\frac{2\pi kn}{N}}$$

$$x(n) = \frac{1}{N}\sum_{k=0}^{N-1} X(k)w^{-kn} \quad \text{with } n \in [0 \dots N-1]$$

In 1965, J.W. Cooley and J.W. Tukey described an algorithm allowing to rapidly calculate the Fourier transform of a series of N complex numbers in an article entitled "An Algorithm for the Machine Calculation of Complex Fourier Series", Math.

Computation, Vol. 19, 1965, pp. 297-301. This algorithm is very interesting when N is a power of two because it is then particularly straightforward to implement. This algorithm requires μ calculation steps where $\mu=\log_2(N)$. It is based on breaking down the series to be transformed into two interleaved sub-series. There are two kinds of interleaving: time interleaving and frequency interleaving. Both kinds of interleaving are explained more in detail in the course of the description with reference to Figs. 1A and 1B.

Specific algorithms have been developed from this basic algorithm to deal with the case of real number series. The Fourier transformation of a series of 8 real numbers according to a time interleaving algorithm and according to a frequency interleaving algorithm is illustrated in Figs. 1A and 1B. For each time interleaving Fourier transform algorithm there is a frequency interleaving algorithm, which corresponds to a double inversion of the series of transformation operations, on the one hand, and for each butterfly circuit, of the proper transformation operations, on the other hand. Whatever the interleaving chosen, the transformation method requires three transformation steps E0, E1, and E2, these steps being implemented through a set of four butterfly circuits CC, generally known as "butterflies" in technical speak. Each butterfly, represented in the figure by a point of intersection between two columns of numbers, performs calculations on two numbers, real or complex. The symbols R and C respectively identify a real number and a complex number. The sequence of real and/or complex

numbers after the transformation steps depends on the interleaving chosen.

The time interleaving algorithm is generally chosen for calculating the Fourier transform of a series of real numbers because of the symmetrical distribution of real and complex numbers throughout the steps. On the other hand, the frequency interleaving algorithm is more suitable for the direct or inverse transformation of a series of conjugated complex numbers.

When the series to be transformed x(n) is real, the Fourier transform verifies the following relation:

x(n) is real if and only if

$$X(n) = X^*(-n) = X^*(N-n); \tag{1}$$

where * designates the conjugating operation.

For a series x(n) of N real numbers, the following results are inferred from this relation:

- X(0) and X(N/2) are real;
- $X(n) = X^*(N-n)$ for $1 \le n \le N/2 - 1$

Relation (1) highlights the presence of redundant information in the X(n) series.

It should be noted that the transformation method is generally implemented by a single set of butterflies, the operating mode of which is modified as the transformation goes along. At each change of operating mode, results are stored in a memory having N storage locations, the output samples of a butterfly replacing the corresponding input samples of the same rank in the memory. This method of applying the algorithm is generally known as the "in place" method. This method has a major advantage: if the elements of

the x(n) series are processed in the first transformation step in bit-reversed order of index n, the numbers of the X(n) series are output in the last transformation step in ascending order of index n and vice versa.

A known transformation method is shown by way of example in Fig. 2. This method performs the Fourier transformation of a real x(n) series according to a complex time interleaving algorithm. In this example, the x(n) series to be transformed comprises sixteen real samples, x(0) to x(15). The transformation method comprises four transformation steps E_p with $0 \leq p \leq 3$. The samples of the x(n) series are shown in the first transformation step in bit-reversed order of their index n.

At this stage of the explanations, the terms used in the course of the description should be defined. The rank of a sample is taken from the position it occupies in the series of samples to which it belongs. The index of a sample then corresponds to the starting rank of this sample.

The intermediate results obtained in the various transformation steps are represented by the series A(n), B(n), and C(n). The samples of the series x(n), A(n), B(n), C(n), and X(n) are stored in double storage locations, one storage location being reserved for the real portion of the sample and the other location being reserved for the imaginary portion thereof. $A_R(n)$ and $A_I(n)$ respectively designate the real portion and the imaginary portion of the index n sample of the A(n) series. Butterflies are represented in the figure by

points of intersection between columns of storage locations. Each butterfly is assigned a coefficient W^s symbolized in Fig. 2 by a pair of coordinates A/B where A and B respectively designate the real portion and the imaginary portion of the coefficient W^s. Coordinates 1/0 and 0/-1 are respectively assigned to coefficients $W^0=1$ and $W^{N/4}=W^4=-j$. For the sake of clarity and in order to simplify their formulation, the remaining coefficients W^s have been represented by the following pairs:

W^1 -> 2 / -4 $\quad$ W^5 -> -4 / -2

W^2 -> 3 / -3 $\quad$ W^6 -> -3 / -3

W^3 -> 4 / -2 $\quad$ W^7 -> -2 / -4

These pairs of coordinates are graphically represented in Fig. 3. In fact, coordinates A and B respectively represent a cosine value and a sine value. This coefficient W^s participates in the calculation performed by the butterfly. Furthermore, the butterflies are distributed at each transformation step among $N/2^{p+1}$ calculation blocks, each calculation block comprising 2^p butterflies. In the course of the description, the parameter q designates the rank of the calculation blocks within the same transformation step; q is included between 0 and $(N/2^{p+1})-1$.

In the first transformation step E_0, the butterflies are distributed among eight calculation blocks, each comprising a butterfly performing an operation on two complex or real samples. If e1 and e2 are to designate the samples applied to the inputs of a butterfly, the latter outputs samples s1 and S2 defined as follows:

$s1 = e1 + W^S.e2$ and $s2 = e1 - (W^S.e2)$

where W^S is the coefficient assigned to said butterfly.

For this first transformation step, coefficient $W^0=1$ is assigned to the eight butterflies. As the samples x(n) and the coefficient W^0 are real, the samples A(n) obtained at the end of step E_0 are real.

For the second transformation step, E_1, the butterflies are distributed among four calculation blocks each comprising two butterflies. Coefficient $W^0=1$ is assigned to the first one of these butterflies; thus, the first butterfly of each calculation block provides two real samples. The second butterfly of the calculation blocks is associated with coefficient $W^{N/4}=W^4=-j$ and generates two conjugated complex samples. The output samples obtained at the end of step E_1 are designated by the series B(n).

For the third transformation step, E_2, the butterflies are distributed among two calculation blocks each comprising four butterflies respectively associated with coefficients W^0, W^2, W^4, and W^6. The output samples in step E_2 are designated by the series C(n). Finally, for the fourth transformation step, E_3, a single calculation block comprising eight butterflies respectively associated with coefficients W^0, W^1, W^2, W^3, W^4, W^5, W^6, and W^7, is provided. This transformation step generates the transformed series X(n).

Given relation (1), the X(n) series comprises, on the one hand, real samples, X(0) and X(8), and on the other hand, complex samples, X(1) to X(7) and X(9) to X(15), samples X(15) to X(9) respectively being the conjugates of samples X(1) to X(7). The X(n) series

therefore contains redundant information. The storage locations outlined in thick stroke in Fig. 2 designate the storage locations enclosing the conjugated values of the complex samples contained in the storage locations associated therewith by an arrow. The intermediate results series B(n) and C(n) also contain redundant information.

It is then possible to delete this redundant information in order to reduce by half the size of the sample storage memory as well as the number of butterflies.

However, removing redundant information stored in the storage locations outline in bold strokes in Fig. 2 implies the complete reorganization of the transformation steps of Fig. 2. Thus reorganizing the transformation has the effect of modifying the output sequence of samples X(n).

The problem therefore consists in reducing the size of the storage memory and the number of butterflies while maintaining the output sequence of the X(n) samples. It is the object of the invention to offer a method of calculating the fast Fourier transform or the inverse fast Fourier transform of a series of N real samples x(n), with N power of 2, operating according to a time interleaving algorithm, which provides the series of samples X(n) in ascending order of index n and uses limited calculation and storage means.

For this purpose, the object of the invention is a method of calculating the fast Fourier transform or the inverse fast Fourier transform of a digital signal

defined by a series of N real starting samples x(n), with N power of two and $n \in [0..N-1]$, comprising successive transformation steps for transforming input samples into output samples, all the transformation steps being performed by means of a single set of butterflies with several inputs and several outputs, the operating mode of which is modified selectively in each transformation step, the input and output samples of each transformation step being stored in a storage memory, a series of N output samples y(n) representative of the fast Fourier transform or the inverse fast Fourier transform of the starting samples x(n) being provided in the last transformation step,

characterized in that output samples y(n) are real,

and in that the output samples of a butterfly replace the corresponding input samples of the same rank in the storage memory, so that, if the starting samples x(n) processed in the first transformation step are classified in bit-reversed order of their index n, output samples y(n) are provided in the last transformation step in ascending order of index n, these output samples being defined by the following relations:

$y(0) = \mathrm{Re}[X(0)]$

$y(n) = \mathrm{Re}[X((n+1)/2)]$ for n being odd and different from N-1

$y(n) = \mathrm{Im}[X(n/2)]$ for n being even and different from 0

$y(N-1) = \mathrm{Re}[X(N/2)]$

where the X(n) samples, with $n \in [0..N-1]$ designate the complex samples of the series corresponding to the fast or inverse fast Fourier transform of the series of starting samples x(n).

For methods operating according to a frequency interleaving algorithm, the invention also relates to a method of calculating the fast Fourier transform or the inverse fast Fourier transform of a digital signal defined by a series of N complex samples X(n) conjugated by pairs represented by a series of N real starting samples y(n), with N power of two and $n \in [0..N-1]$, the starting samples y(n) being defined as follows:

$y(0) = \mathrm{Re}[X(0)]$

$y(n) = \mathrm{Re}[X((n+1)/2)]$ for n being odd and different from N-1

$y(n) = \mathrm{Im}[X(n/2)]$ for n being even and different from 0

$y(N-1) = \mathrm{Re}[X(N/2)]$

this calculation method comprising successive transformation steps for transforming input samples into output samples, a series of N output samples x(n) representative of this fast or inverse fast Fourier transform being provided in the last transformation step, all the transformation steps being performed by means of a single set of butterflies with several inputs and several outputs, the operating mode of which is modified selectively in each transformation step, the input and output samples of each transformation step being stored in a storage memory,

characterized in that output samples $x(n)$ are real,

and in that the output samples of a butterfly replace the corresponding input samples of the same rank in the storage memory, so that, if the starting samples $y(n)$ processed in the first transformation step are classified in ascending order of index n, the output samples $x(n)$ are output in the last transformation step in bit-reversed order of index n.

The inventive calculation methods perform operations on real samples and consequently use limited calculation and storage means in comparison with the method shown in Fig. 2.

According to another feature of the invention, in each transformation step, the butterflies transform input sample pairs, the ranks of the input samples of the same pair within the series of input samples of said transformation step being symmetrical with respect to a center between the end rank values of the input samples transformed by said butterfly. Input samples processed by the same butterfly are thus symmetrically linked together by pairs. The result is simplified handling of sample addressing.

According to another aspect of the invention, the method preferably comprises $\mu-1$ transformation steps E_p with $\mu=\log_2(N)$ and $p \in [0..\mu-2]$.

Other features and advantages of the invention will be apparent from reading the following detailed description, which is made with reference to the appended drawings, where:

- Figs. 1A and 1B, already described, respectively represent a Fourier transformation of eight real numbers according to a time interleaving algorithm and according to a frequency interleaving algorithm;

- Fig. 2, already described, illustrates the transformation of a series of 16 real numbers into a series of 16 complex numbers according to a complex time interleaving algorithm;

- Fig. 3, already described, graphically represents the mapping of coefficients W^s and coordinate pairs A/B;

- Fig. 4 represents modifications applied to part of the transformation of Fig. 2;

- Fig. 5 illustrates a modified transformation only processing real numbers;

- Fig. 6 is a representation of the method of calculating the fast Fourier transform according to the invention;

- Figs. 7A and 7B respectively illustrate permutations performed on peripheral butterflies and on internal butterflies with odd-numbered rank of the transformation of Fig. 5;

- Fig. 8 represents an embodiment of the transformation method according to the invention, comprising μ transformation steps;

- Fig. 9 illustrates a grouping of peripheral butterflies according to a first embodiment of a transformation method comprising $\mu-1$ transformation steps;

- Fig. 10 represents a first embodiment of a transformation method comprising μ-1 transformation steps;

- Fig. 11 represents a butterfly design relating to the transformation method illustrated in Fig. 10;

- Fig. 12 represents an alternative of the embodiment of Fig. 10;

- Fig. 13 represents a butterfly design relating to the embodiment of Fig. 12;

- Fig. 14 illustrates a grouping of peripheral butterflies according to a second embodiment of a transformation method comprising μ-1 transformation steps;

- Fig. 15 represents a second embodiment of a transformation method comprising μ-1 transformation steps, with μ being even;

- Fig. 16 represents an alternative of the preceding embodiment, with μ being odd;

- Fig. 17 represents a butterfly design relating to the embodiments shown in Figs. 15 and 16;

- Fig. 18 represents a third embodiment of a transformation method comprising μ-1 transformation steps;

- Fig. 19 represents a butterfly design relating to the embodiment of Fig. 18;

- Fig. 20 represents the addresses that are associated with the various butterflies implemented in the embodiment shown in Fig. 12;

- Fig. 21 represents the addresses that are associated with part of the samples of a transformation method processing a series of 32 real samples;

- Fig. 22 represents a sample embodiment of a transformation method operating according to a frequency interleaving algorithm;

- Fig. 23 represents a butterfly design relating to the embodiment of Fig. 22;

- Fig. 24 represents the addresses that are associated with the various butterflies of the embodiment illustrated in Fig. 22.

According to the invention, only one part of the X(n) samples is calculated, the other part of the samples being redundant. E.g., the calculation could be limited to the X(n) numbers for $0 \leq n \leq N/2$. However, a more adequate solution is calculating the X(n) numbers for $0 \leq n \leq N/4$ and $N/2 \leq n \leq 3N/4$. The latter solution is preferable because it only involves the first (N/4)+1 butterflies for calculating X(n), as the last (N/4)-1 butterflies can be removed. The storage locations thus released can be used for storing the real portion or the imaginary portion of the remaining X(n) numbers. The size of the storage memory can thus be halved if the size of the storage locations is limited to storing a real number instead of a complex number. The real portion of the complex X(n) number is stored in the storage location that has been assigned thereto initially whereas the imaginary portion is stored in the storage location initially assigned to the number X(N-n). The same operation can be performed for the series of intermediate results B(n) and C(n). The transformation method of the invention is limited to calculating N real samples y(n) instead of N complex samples. The real samples y(n) are defined as follows:

y(0) = Re[X(0)]

y(n) = Re[X((n+1)/2)] for n being odd and different from N-1

y(n) = Im[X(n/2)] for n being even and different from 0

y(N-1) = Re[X(N/2)].

Removing redundant information and reorganizing storage locations deeply modifies the diagram of Fig. 2. Fig. 4 illustrates the reorganizations performed on the part relating to the calculation of samples A(1), A(3), A(5), A(7), B(1), B(3), B(5), B(7), C(1), C(3), C(5), and C(7). Redundant samples to be removed are B(3), B(7), C(5), and C(7). When the size of the storage locations has been reduced and the samples remaining in theses storage locations have been reorganized, the butterflies perform calculations on real numbers. The butterflies to which the pair 0/-1 is assigned perform calculations on two real numbers. In practice, they copy onto their first output the number present at their first input and multiply by -1 the number present at their second input and provide it to their second output. The butterflies to which the pair 1/0 is assigned perform an addition and a subtraction on two real numbers. Finally, the other butterflies perform operations on four real numbers.

The transformation thus rearranged is illustrated in Fig. 5. In this figure, the butterflies associated with the pairs 1/0 and 0/-1 corresponding to the coefficients W^0 and W^4 are connected with storage locations by thick strokes. This figure shows that the reorganization of the transformation method steps

modifies the output sequence of the y(n) samples and therefore the output sequence of the X(n) series. Furthermore, this transformation method no longer has a specific symmetry allowing to link by pairs the samples processed by the same butterfly. The result is a very complicated address management of the samples to be applied to the butterfly inputs.

Fig. 6 shows the steps of a method of calculating the fast Fourier transform or the inverse fast Fourier transform of a series of N real numbers x(n), with N power of 2, operating according to a time interleaving algorithm. It mainly comprises transformation steps 2 for transforming N starting samples x(n) classified in bit-reversed order of their index n into real output samples y(n) representative of this Fourier transform classified in ascending order of index n.

Advantageously, it comprises a preliminary step 1 for ranking the N real starting samples x(n) to be transformed in the bit-reversed order of their index n if the samples x(n) are not already in this sequence and a final step 3 for generating the N complex samples X(n) corresponding to the fast Fourier transform of the starting samples x(n) from the N real samples y(n) obtained at the end of the transformation steps.

The methods that will be detailed in the course of the description will be more in particular for calculating the fast Fourier transform of a real series. Also, the coefficients W^s assigned to the butterflies for implementing the inventive method will be of the type $e^{-j(2\pi s/N)}$ with $s \geq 0$. For calculating the inverse fast Fourier transform the calculation method

is the same, however, the coefficient is of the type $e^{j(2\pi s/N)}$ with $s \geq 0$.

In order to obtain at the same time y(n) samples sorted in ascending order of index n and symmetry of calculation, according to the invention, it is suggested to modify the calculations performed by the butterflies of the odd rank q calculation blocks of the transformation illustrated in Fig. 5 in accordance with the diagrams of Figs. 7A and 7B.

As the butterflies associated with coefficient 1/0 (Fig. 7A) of the odd rank calculation blocks are involved, provision is made for permutation both outputs of the butterfly and multiplying by -1 the result provided at the second butterfly output.

As the four input butterflies (Fig. 7B) are involved, provision is made for permutation the first two outputs with the last two ones.

This method is applied to the whole transformation and then a method is obtained providing at the output y(n) samples in ascending order of index n. This method is illustrated in Fig. 8. The symbol $\circlearrowleft$ placed above the calculation blocks designates the calculation blocks wherein butterflies have been modified, i.e. odd rank calculation blocks. Due to the symmetry of calculation of the transformation method, the intersecting points representing the butterflies are superposed inside each calculation block.

The butterflies to which the coefficient 1/0 is assigned are called peripheral butterflies because they perform calculations on samples arranged at the ends of the calculation block. The other butterflies are called

internal butterflies. It should be noted that in each transformation step, not all the samples are processed always and that unprocessed samples are kept in their storage locations to be processed in subsequent steps, or else produced as output if they are already in their final shape.

The transformation method thus modified provides y(n) samples in ascending order of index n and, in each transformation step, has symmetry of calculation facilitating the addressing of the samples to be processed.

According to another aspect of the invention, the calculation method advantageously comprises μ-1 transformation steps. Several embodiments derived from that of Fig. 8 and comprising μ-1 transformation steps are therefore shown in the course of the description. One butterfly design is associated with each of these embodiments.

All of these embodiments have the following features in common:

- in each transformation step, provision is made for $N/2^{p+2}$ calculation blocks and each calculation block comprises an peripheral butterfly and/or 2^p-1 internal butterflies; all butterflies, be they peripheral or internal, perform calculations on four real samples;

- the ranks of the samples processed by the same butterfly are defined as follows: if, in transformation step E_β, a peripheral butterfly belonging to the rank α calculation block is taken into consideration, it transforms the input samples of rank $2^{\beta+2}\alpha$, $2^{\beta+2}\alpha+2^{\beta+1}-1$, $2^{\beta+2}\alpha+2^{\beta+1}$, $2^{\beta+2}\alpha+2^{\beta+2}-1$ into output samples of the same

rank, and, if an internal rank τ butterfly in a rank α calculation block is taken into consideration in step E_β, it transforms the input samples of rank $2^{\beta+2}\alpha+2\tau+1$, $2^{\beta+2}\alpha+2\tau+2$, $2^{\beta+2}\alpha+2^{\beta+2}-2\tau-3$, $2^{\beta+2}\alpha+2^{\beta+2}-2\tau-2$ into output samples of the same rank, with $\beta\geq1$;

- the coefficient assigned to the internal rank τ butterfly of the rank α calculation block in step E_β is equal to W^δ with $\delta = (\tau+1).(N/2^{\beta+2})$.

In all of these embodiments, the input samples of each butterfly form sample pairs, the sample ranks of the same pair in the series of input samples of a transformation step being symmetrical with respect to the center value of the end ranks of the input samples transformed by said butterfly. This center corresponds to the value $2^{\beta+2}\alpha+2^{\beta+1}-1/2$. Therefore, one just has to know the rank of two of the four samples to be applied to the inputs of the butterfly in order to infer therefrom the rank of the other two. Addressing these samples is thus simplified. This will be explained more in detail at a later point of the description.

Thus, according to a first embodiment, the neighboring calculation blocks in each transformation step are grouped by pairs. The peripheral butterflies of the same calculation block are then merged into a single peripheral butterfly. A sample merging of two peripheral butterflies is shown in Fig. 9. This example relates to peripheral butterflies associated with samples x(0), x(8), x(4), and x(12).

Furthermore, as the transformation steps do not process all samples each time, certain calculations can

be anticipated. E.g., calculating samples $C_R(1)$, $C_I(1)$, $C_R(3)$, and $C_I(3)$ can be done in the second transformation step. The result is the diagram of Fig. 10 showing a first embodiment of the transformation circuit wherein the method only comprises $\mu-1$ transformation steps. In transformation step E_p, each calculation block has a peripheral butterfly and 2^p-1 internal butterflies. It can be considered that this method only comprises 3 transformation steps, the fourth step being limited to performing an addition and a subtraction. This addition and this subtraction are preferably performed during the final step, and in order to limit the number of transformation steps.

A butterfly design associated with the embodiment of Fig. 10 is represented in Fig. 11. It comprises:

- four inputs for receiving input samples e1, e2, e3, e4, and four outputs for providing output samples s1, s2, s3, s4, and
- three additional, respectively primary mode MP, permutation PERM, and coefficient COEF, inputs.

This butterfly is responsible for selectively applying to input samples e1, e2, e3, and e4, various transformation operations each determined by the values assigned to primary mode, permutation signals and to coefficient W^s admitted at the corresponding additional inputs.

The primary mode signal is 0 for a peripheral butterfly and 1 for an internal butterfly. When the permutation signal is a 1, the output samples s1 and s2 of the butterfly are swapped with output samples s3 and s4. This permutation is only possible if the butterfly

is an internal one. Finally, the coefficient W^s associated with the butterfly is applied to the coefficient input COEF.

Thus, if the complex coefficient W^s=A+j.B is applied to the coefficient entry of the butterfly, the latter provides the following output samples s1, s2, s3, and s4

1) if the primary mode signal is 0:

s1 = e1 + e2

s2 = e1 - e2

s3 = e4 - e3

s4 = e3 + e4

2) if the primary mode signal is 1 and the permutation signal is 0:

s1 = e1 + A.e3 - B.e4

s2 = e2 + B.e3 + A.e4

s3 = e1 - A.e3 + B.e4

s4 = -e2 + B.e3 + A.e4

3) if the primary signal is 1 and the permutation signal is 1:

s1 = e1 - A.e3 + B.e4

s2 = -e2 + B.e3 + A.e4

s3 = e1 + A.e3 -B.e4

s4 = e2 + B.e3 + A.e4

According to an alternative embodiment, provision can be made for addition and subtraction to be performed inside the peripheral butterfly of the last transformation step. This embodiment is shown in Fig. 12. For this purpose, the corresponding butterfly design has a fourth additional input called secondary mode input MS to which a secondary mode signal is

applied. This signal is 1 for the peripheral butterfly of the last transformation step, otherwise it is 0. This design is illustrated in Fig. 13. This design has an additional operating mode in comparison with the preceding one; thus, when the primary mode signal is 0 and the secondary mode signal is 1, the output obtained is:

$$s1 = e1 + e2 + e3 + e4$$
$$s2 = e1 - e2$$
$$s3 = e4 - e3$$
$$s4 = (e1 + e2) - (e3 + e4)$$

According to a second embodiment derived from the diagram of Fig. 8, the neighboring peripheral butterflies in even index p transformation steps E_p are grouped by pairs and are merged with the peripheral butterfly of the second odd index step in order to form a new peripheral butterfly at the odd index step. This grouping is illustrated in Fig. 14 in an example. In this example, the peripheral butterflies of the first transformation step processing samples x(0), x(8), x(4), and x(12) are merged with the peripheral butterfly of the second step processing samples $A_R(0)$ and $A_R(2)$. Also, the peripheral butterflies of the first transformation step processing samples x(2), x(10), x(6), and x(14) are merged with the peripheral butterfly of the second step processing samples $A_R(4)$ and $A_R(6)$. The two butterflies obtained are different in that the second one performs in addition a permutation between the first and second outputs. If this grouping is applied to the whole transformation illustrated in Fig. 8, the result is that step E_0 no

longer uses any butterflies and can be removed. The resulting transformation method is illustrated in Fig. 15.

However, two cases should be distinguished for this transformation method: the case where N is an even power of two (μ being even) and the case where N is an odd power of two (μ being odd).

In case μ is even, there is an even number of transformation steps in the embodiment shown in Fig. 8 and grouping the peripheral butterflies of even index steps with those of the following odd index steps is no problem. This case corresponds to the diagram in Fig. 15.

In case μ is odd, the peripheral butterfly of the last even index step cannot be grouped with other peripheral butterflies. Therefore, a specific operating mode should be provided for this case. This case is illustrated in Fig. 16, this figure representing the transformation of a series of eight real samples (μ=3). The peripheral butterfly of the last transformation step of this circuit could not be merged with other peripheral butterflies.

The butterfly design associated with this second embodiment is illustrated in Fig. 17; it differs from the preceding design in that the secondary mode signal is 1 when a peripheral butterfly for implementing the last step is involved and μ is odd, and in that permutation applies to all the butterflies of the even rank calculation blocks.

The calculations performed by the butterfly are also different and are defined as follows:

1) if primary mode, secondary mode and permutation signals are 0:

$$s1 = e1 + e2 + e3 + e4$$
$$s2 = e1 - e2$$
$$s3 = e4 - e3$$
$$s4 = (e1 + e2) - (e3 + e4)$$

2) if the primary mode signal is 0 and the secondary mode signal is 1:

$$s1 = e1 + e4$$
$$s2 = e2$$
$$s3 = e3$$
$$s4 = e1 - e4$$

3) if the primary mode signal is 0 and the permutation signal is 1:

$$s1 = (e3 + e4) - (e1 + e2)$$
$$s2 = e1 - e2$$
$$s3 = e4 - e3$$
$$s4 = e1 + e2 + e3 + e4$$

4) if the primary mode signal is 1 and the permutation signal is 0:

$$s1 = e1 + A.e3 - B.e4$$
$$s2 = e2 + B.e3 + A.e4$$
$$s3 = e1 - A.e3 + B.e4$$
$$s4 = -e2 + B.e3 + A.e4$$

5) if the primary mode signal is 1 and the permutation signal is 1:

$$s1 = e1 - A.e3 + B.e4$$
$$s2 = -e2 + B.e3 + A.e4$$
$$s3 = e1 + A.e3 - B.e4$$
$$s4 = e2 + B.e3 + A.e4$$

In case μ is odd, it is also possible on the one hand to provide for the peripheral butterflies to be grouped for implementing the first transformation step in the same way as in the first embodiment, and on the other hand, for the butterflies of the other steps to be grouped as in the third embodiment. Grouping butterflies from the second step on is then performed by assigning an even index to the first transformation step. These groupings are represented in Fig. 18.

The butterfly design corresponding to this embodiment is represented in Fig. 19. The secondary mode signal is 1 for a peripheral butterfly implementing the first transformation step of the circuit and if μ is even. The calculations performed by this butterfly are the following ones:

1) if the primary mode signal is 0 and the secondary mode signal is 1:

$s1 = e1 + e2$

$s2 = e1 - e2$

$s3 = e4 - e3$

$s4 = e3 + e4$

2) if primary mode, secondary mode and permutation signals are 0:

$s1 = e1 + e2 + e3 + e4$

$s2 = e1 - e2$

$s3 = e4 - e3$

$s4 = (e1 + e2) - (e3 + e4)$

3) if the primary mode and secondary mode signals are 0 and the permutation signal is 1:

$s1 = (e3 + e4) - (e1 + e2)$

$s2 = e1 - e2$

s3 = e4 − e3

s4 = e1 + e2 + e3 + e4

4) if the primary mode signal is 1 and the permutation signal is 0:

s1 = e1 + A.e3 − B.e4

s2 = e2 + B.e3 + A.e4

s3 = e1 − A.e3 + B.e4

s4 = −e2 + B.e3 + A.e4

5) if the primary mode signal is 1 and the permutation signal is 1:

s1 = e1 − A.e3 + B.e4

s2 = −e2 + B.e3 + A.e4

s3 = e1 + A.e3 − B.e4

s4 = e2 + B.e3 + A.e4

In all the embodiments described before, the ranks of samples processed by the same butterfly are symmetrical by pairs with respect to a center value. One just has to know the rank of the first two input samples of the butterfly to infer the other two ones therefrom by symmetry. If the input and output samples are saved in storage locations the address of which corresponds to the rank of these samples, addressing the latter will be simplified.

Indeed, all that needs to be done then is to generate two addresses per butterfly, as the other two ones can be inferred by symmetry. Furthermore, it should be noted that the addresses of the input samples and those of the corresponding output samples are the same because the transformation is applied according to an in place method.

The addresses associated with the various butterflies relating to the transformation method of Fig. 12 are shown in Fig. 20. The address of a sample is taken to be equal to the rank of this sample in the sample series to which it belongs. The series to be transformed in the example of Fig. 12 comprises 16 samples. Therefore, 16 addresses have to be produced, with four-bit addressing. For a series of N samples, $\log_2(N)$-bit addressing is performed.

Each transformation step of the method of Fig. 12 is implemented by four butterflies each processing four real input samples. The binary addresses of the samples to be processed in each step are therefore distributed among four groups of four addresses. The address groups relating to one peripheral butterfly are contained in bold line boxes and the address groups relating to an internal butterfly are contained in thin line boxes. Furthermore, the address groups relating to the same calculation block are grouped in dotted line boxes.

The first peripheral butterfly in step E_0 processes the first four samples of the x(n) series resulting from the preliminary classification step. The corresponding addresses to be generated for this butterfly are therefore 0000, 0001, 0010, and 0011. Also, considering the first peripheral butterfly of step E_1, it processes the samples of rank 0, 3, 4, and 7 of the A(n) series. The corresponding addresses to be generated for this butterfly are therefore 0000, 0011, 0100, and 0111.

The $(\mu-2-p)$ high-order bits of the addresses indicate rank q of the calculation block to which the

address is related. Thus, the two high-order bits of the addresses generated for the first calculation block of step E_0 are 00. It should also be noted that, as the last step only uses a single calculation block, the addresses generated for this calculation block have no bit relating to the rank of this block (μ-p-2=0).

For each butterfly, provision is made for generating only two binary addresses; the other two ones are obtained by inverting the (p+2) low-order bits of the generated addresses. E.g., considering the addresses of the first peripheral butterfly in step E_1, only addresses 0000 and 0011 are generated and addresses 0111 and 0100 are obtained by inverting the 3 low-order bits of the generated addresses.

A first and a second address per butterfly are then produced, these addresses being consecutive for an internal butterfly. When a peripheral butterfly is involved, the p+2 low-order bits of the first address are equal to 0, and the p+2 low-order bits of the second address form a number equal to $2^{p+1}-1$.

Regarding the coefficients W^s to be applied to the coefficient inputs COEF of the internal butterflies, they are stored in a memory of the calculation circuit. Only N/4 coefficient values are required for calculating the Fourier transform. μ-2-bit addressing is performed for these coefficients. In the embodiments described previously where N=16, only the pairs 1/0, 2/-4, 3/-3, and 4/-2 are used which correspond to coefficients W^0, W^1, W^2, W^3.

According to the invention, the address of these four coefficients must therefore be known. The address

associated with each coefficient W^s is chosen to be equal to the value of the power s. Consequently, the addresses of coefficients W^0, W^1, W^2, W^3 are 00, 01, 10, 11, respectively.

So as not to be obliged to generate these addresses, according to the invention, the addresses produced for addressing the samples are used. The address of the coefficient which is assigned to a butterfly is included in the second address produced corresponding to the greater one of both addresses.

However, we must distinguish between three cases:

a) when $p+1=\mu-2$, the coefficient address corresponds to the number formed by the p+1 low-order bits of the second address generated for this internal butterfly. This is the case for the second step (p=1) in the example of Fig. 20. The 2 low-order bits of the second address are 10 and therefore designate coefficient W^2.

b) if $p+1>\mu-2$, the coefficient address corresponds to the number formed by the p+1 low-order bits of the second address generated for this internal butterfly, minus its $\mu-p-1$ low-order bits. This is the case for the third step (p=2) in the example of Fig. 20. The 3 low-order bits of the second address generated for the first internal butterfly are 010. When the last bit ($\mu-p-1=1$) of this number is taken away, the number 01 is obtained relating to coefficient W^1. This case always corresponds to the last step of a transformation method comprising $\mu-1$ transformation steps.

c) if $p+1<\mu-2$, the coefficient address corresponds to the number formed by the p+1 low-order bits of the

second address generated for this internal butterfly, followed by μ-p-3 zero bits at the end of the number. This case is illustrated in Fig. 21. This figure represents the addresses relating to an internal butterfly for implementing the second transformation step (p=1) of a transformation method designed for processing a series of 32 real samples (N=32 and μ=5). This butterfly transforms the samples of rank 1, 2, 5, and 6 of the series of samples obtained at the end of the first step. The two low-order bits of the second generated address are 10 and if a zero (μ-p-3=1) is added at the end of the number, the number 100 is obtained designating coefficient W^4.

Thus, both addresses produced by the address generator for one butterfly is used to address both four samples to be processed and the coefficient relating to the butterfly.

Preferably, even address and odd address samples will be stored in two separate memories. Thus, it will be possible to read two input samples simultaneously, and it will be possible to write the resulting output samples simultaneously, which means saving process time for the series to be transformed.

As mentioned before, for each calculation method operating according to a time interleaving algorithm there is a corresponding method operating according to a frequency interleaving algorithm. All that has to be done to obtain it is, on the one hand, to invert the series of transformation operations of the corresponding time interleaving method, and on the other hand, for each butterfly, to invert the

transformation operations themselves with respect to those of the corresponding method.

In addition, the invention also relates to a method of calculating the fast Fourier transform or the inverse fast Fourier transform of a series of N complex samples X(n) conjugated by pairs, with N power of 2, operating according to a frequency interleaving algorithm. The series of N complex samples X(n) is represented by a series of N real samples y(n) defined as follows:

$y(0) = \mathrm{Re}[X(0)]$

$y(n) = \mathrm{Re}[X((n+1)/2)]$ for n being odd and different from N-1

$y(n) = \mathrm{Im}[X(n/2)]$ for n being even and different from 0

$y(N-1) = \mathrm{Re}[X(N/2)]$

According to the invention, this method substantially comprises transformation steps for transforming input samples into output samples. Real samples y(n) are processed in a first transformation step and the last step provides a series of N output samples x(n) representative of the fast or the inverse fast Fourier transform of the sample series X(n). As for the time interleaving circuits, each transformation step is implemented by a set of butterflies with several inputs and several outputs. As the transformation is done according to an in place method, all the steps are performed by means of a single set of butterflies, the operating mode of which is modified in each transformation step. In each transformation step, input and output samples are stored in a storage

memory. When they have been transformed, the output samples of the same butterfly replace the corresponding input samples of the same rank in the storage memory.

According to the invention, if the samples y(n) introduced in the first transformation step are classified in ascending order of index n, the output samples x(n) are provided in the last transformation step in bit-reversed order of index n. The output sequence of the x(n) samples can then be modified by a final step so as to classify them in ascending order of index n.

An embodiment of such a transformation is represented in Fig. 22. It is inferred from the embodiment of Fig. 12 by inverting the functional arrangement of the transformation steps of Fig. 12 (mirror image of what it is for time interleaving). This embodiment allows the calculation of the inverse fast Fourier transform of a series of 16 real samples y(n) representative of a series of 16 complex samples X(n) conjugated by pairs. Coefficients W^s are therefore of the type $e^{j(2\pi s/N)}$.

This embodiment comprises three transformation steps E_p with $0 \leq p \leq 2$. In each transformation step E_p, the butterflies are henceforth distributed among 2^p calculation blocks, these calculation blocks being sorted in each step according to an ascending rank q from 0 to 2^p-1. Each calculation block has a peripheral butterfly and $N/2^{p+2}-1$ internal butterflies. The y(n) samples are applied in the sequence of index n in the first step.

The ranks of the samples processed by the same butterfly are defined as follows: in step E_β, considering a peripheral butterfly belonging to the rank α calculation block, it transforms the input samples of rank $2^{\mu-\beta}\alpha$, $2^{\mu-\beta}\alpha+2^{\mu-\beta-1}-1$, $2^{\mu-\beta}\alpha+2^{\mu-\beta-1}$, $2^{\mu-\beta}\alpha+2^{\mu-\beta}-1$ into output samples of the same rank, and, in step E_β, considering an internal rank τ butterfly in the rank α calculation block, it transforms the input samples of rank $2^{\mu-\beta}\alpha+2\tau+1$, $2^{\mu-\beta}\alpha+2\tau+2$, $2^{\mu-\beta}\alpha+2^{\mu-\beta}-2\tau-3$, $2^{\mu-\beta}\alpha+2^{\mu-\beta}-2\tau-2$ into output samples of the same rank. Finally, the coefficient assigned to the rank τ internal butterfly of the rank α calculation block in step E_β is equal to W^δ with $\delta = (\tau+1).2^\beta$.

The real samples x(n) obtained at the end of the method are provided in bit-reversed order of index n.

At the butterflies, the coefficients W^s are of the type $e^{j(2\pi s/N)}$ and inputs and outputs have been inverted with respect to the embodiment of Fig. 12. Consequently, the operations performed by the butterflies of this embodiment are different from those performed by the butterflies of Fig. 12. A butterfly design associated with the embodiment of Fig. 22 is represented in Fig. 23.

Just like the design associated with Fig. 12, it has four data inputs and four data outputs as well as four additional inputs, respectively primary mode MP, secondary mode MS, permutation PERM, and coefficient COEF inputs. The primary mode signal is 0 for an peripheral butterfly and 1 for an internal butterfly. The permutation signal is 0 for even values of rank q

and 1 for odd values. Finally, the secondary mode signal is 1 if the peripheral butterfly is used for implementing the first step, and otherwise 0.

The calculations performed by this butterfly are the following ones ($W^s = A + j.B$):

1) if the primary mode and secondary mode signals are 0:

$$s1 = (e1 + e2)/2$$
$$s2 = (e1 - e2)/2$$
$$s3 = (e4 - e3)/2$$
$$s4 = (e3 + e4)/2$$

2) if the primary mode signal is 0 and the secondary mode signal is 1:

$$s1 = [(e1+e4)/2+e2]/2$$
$$s2 = [(e1+e4)/2-e2]/2$$
$$s3 = -[e3-(e1-e4)/2]/2$$
$$s4 = [e3+(e1-e4)/2]/2$$

3) if the primary mode signal is 1 and the permutation signal is 0:

$$s1 = (e1+e3)/2$$
$$s2 = (e2-e4)/2$$
$$s3 = [(e1-e3).A - (e2+e4).B]/2$$
$$s4 = [-(e1-e3).B + (e2+e4).A]/2$$

4) if the primary mode signal is 1 and the permutation signal is 1:

$$s1 = [(e1-e3).A - (e2+e4).B]/2$$
$$s2 = [-(e1-e3).B + (e2+e4).A]/2$$
$$s3 = (e1+e3)/2$$
$$s4 = (e2-e4)/2$$

This design is inferred from the design of Fig. 13 by inverting values e1, e2, e3, e4 and values s1, s2,

s3, s4, and by replacing B with -B because coefficient W^s is now of the type $e^{j(2\pi s/N)}$. As A and B represent the cosine and sine of the same number, they have become $A^2+B^2=1$. The expressions of s1, s2, s3, s4 are thus simplified.

According to the invention, addresses are furthermore generated for sample addressing. Provision is made for generating two binary addresses of μ bits per butterfly, each binary address corresponding to the rank of a butterfly input sample. The addresses of the other two samples to be applied to the butterfly inputs are obtained by inverting the μ-p low-order bits of the first two addresses.

In the same way as for the transformation methods operating according to time interleaving, the two binary addresses produced are consecutive for an internal butterfly. For a peripheral butterfly, the μ-p low-order bits of the first generated address are equal to 0, and the μ-p low-order bits of the second address form a number equal to $N/2^{p+1}-1$. By way of example, the addresses produced for the transformation circuit of Fig. 22 are gathered in Fig. 24.

Advantageously, provision can be made for storing even address samples and odd address samples in two separate memories in order to reduce processing time of the transformation operation.

Finally, the addresses generated for addressing the samples are also used for addressing coefficients W^s. The value of the parameter s is used for addressing the corresponding coefficient W^s. In this embodiment, the parameter s is equal to:

- if $\mu-p-1=\mu-2$, the number formed by the $\mu-p-1$ low-order bits of the second address produced for said internal butterfly,
- if $\mu-p-1<\mu-2$, the number formed by the $\mu-p-1$ low-order bits of the second address produced for said internal butterfly, followed by $p-1$ zero bits at the end of the number,
- if $\mu-p-1>\mu-2$, the number formed by the $\mu-p-1$ low-order bits of the second address produced for said internal butterfly, minus its $p+1$ low-order bits. This case corresponds to the first step ($p=0$) of the transformation methods operating according to a frequency interleaving algorithm.

CLAIMS

1. A method of calculating the fast Fourier transform or the inverse fast Fourier transform of a digital signal defined by a series of N real starting samples x(n), with N a power of two and n ∈ [0..N-1], comprising successive transformation steps (2) for transforming input samples into output samples, all the transformation steps being performed by means of a single set of butterflies with several inputs and several outputs, the operating mode of which is modified selectively in each transformation step, the input and output samples of each transformation step being stored in a storage memory, a series of N output samples y(n) representative of the fast Fourier transform or the inverse fast Fourier transform of the output samples x(n) being provided in the last transformation step,

characterized in that output samples y(n) are real,

and in that the output samples of a butterfly replace the corresponding input samples of the same rank in the storage memory, so that, if the starting samples x(n) processed in the first transformation step are classified in bit-reversed order of their index n, output samples y(n) are provided in the last transformation step in ascending order of index n, these output samples being defined by the following relations:

$y(0) = Re[X(0)]$

$y(n) = Re[X((n+1)/2)]$ for n being odd and different from N-1

$y(n) = Im[X(n/2)]$ for n being even and different from 0

$y(N-1) = Re[X(N/2)]$

where samples X(n), with $n \in [0..N-1]$, designate the complex samples of the series corresponding to the fast or inverse fast Fourier transform of the starting sample series x(n).

2. A method of calculating the fast Fourier transform or the inverse fast Fourier transform of a digital signal defined by a series of N complex samples X(n) conjugated by pairs represented by a series of N real starting samples y(n), with N power of two and n $\in [0..N-1]$, the starting samples y(n) being defined as follows:

$y(0) = Re[X(0)]$

$y(n) = Re[X((n+1)/2)]$ for n being odd and different from N-1

$y(n) = Im[X(n/2)]$ for n being even and different from 0

$y(N-1) = Re[X(N/2)]$

this calculation method comprising successive transformation steps for transforming input samples into output samples, a series of N output samples x(n) representative of this fast or inverse fast Fourier transform being provided in the last transformation step, all the transformation steps being performed by means of a single set of butterflies with several inputs and several outputs, the operating mode of which

is modified selectively in each transformation step, the input and output samples of each transformation step being stored in a storage memory,

characterized in that output samples x(n) are real,

and in that the output samples of a butterfly replace the corresponding input samples of the same rank in the storage memory, so that, if the starting samples y(n) processed in the first transformation step are classified in ascending order of index n, the output samples x(n) are provided in the last transformation step in bit-reversed order of index n.

3. The calculation method according to claim 1 or 2, characterized in that, in each transformation step, each butterfly transforms input sample pairs, the ranks of the input samples of the same pair within the series of input samples of said transformation step being symmetrical with respect to a center between the end rank values of the input samples transformed by said butterfly.

4. The calculation method according to claim 3, characterized in that it comprises μ-1 transformation steps E_p with $\mu=\log_2(N)$ and $p \in [0..\mu-2]$.

5. The calculation method according to claim 4, in turn dependent on claim 3, in turn dependent on claim 1, characterized in further comprising:

- a preliminary step of modifying the sequence of the starting samples x(n) ranked in ascending order of index n and showing them in bit-reversed order of index n in the first transformation step, and

- a final step of processing the series of output samples y(n) and providing a series of N complex conjugated samples X(n) corresponding to the fast or the inverse fast Fourier transform of the series of starting samples x(n).

6. The calculation method of claim 4, in turn dependent on claim 3, in turn dependent on claim 1, or according to claim 5, characterized in that, in each transformation step E_p, butterflies are distributed among $N/2^{p+2}$ calculation blocks,

in that each calculation block has a peripheral butterfly and/or 2^p-1 internal butterflies,

in that the peripheral butterfly of the rank α calculation block in transformation step E_β transforms the input samples of rank $2^{\beta+2}\alpha$, $2^{\beta+2}\alpha+2^{\beta+1}-1$, $2^{\beta+2}\alpha+2^{\beta+1}$, $2^{\beta+2}\alpha+2^{\beta+2}-1$ into output samples of the same rank,

and in that the internal rank τ butterfly of the rank α calculation block in transformation step E_β transforms the input samples of rank $2^{\beta+2}\alpha+2\tau+1$, $2^{\beta+2}\alpha+2\tau+2$, $2^{\beta+2}\alpha+2^{\beta+2}-2\tau-3$, $2^{\beta+2}\alpha+2^{\beta+2}-2\tau-2$ into output samples of the same rank, with $\beta \geq 1$.

7. The calculation method according to claim 6, characterized in that each butterfly is assigned a coefficient W^s, whereon the calculation inside the butterfly is based, said coefficient being equal to $e^{-j(2\pi s/N)}$ with $s \in [0..N/4-1]$ for a fast Fourier transform and is equal to $e^{j(2\pi s/N)}$ with $s \in [0..N/4-1]$ for an inverse fast Fourier transform.

8. Calculation method according to claim 7, characterized in that the internal rank τ butterfly of

the rank α calculation block in transformation step E_β is assigned coefficient W^δ with $\delta = (\tau+1).(N/2^{\beta+2})$.

9. The calculation method according to claim 8, characterized in that the butterflies for implementing the transformation steps are all of the same type and have

- four inputs for receiving input samples and four outputs for providing output samples,

- four additional inputs, respectively primary mode, secondary mode, permutation, and coefficient inputs,

in order to selectively apply different transformation operations to the input samples, each operation being determined by the values assigned to the primary mode, secondary mode, permutation signals, and a coefficient admitted on said corresponding additional inputs.

10. The calculation method according to claim 9, characterized in that, for each butterfly, the primary mode signal is 0 for a peripheral butterfly and 1 for an internal butterfly,

in that the permutation signal is 0 for the even rank calculation blocks, including rank 0, and 1 for the other ones.

11. The calculation method according to claim 10, characterized in that, in transformation step Ep, each calculation block comprises one peripheral butterfly and 2^p-1 internal butterflies.

12. The calculation method according to claim 11, characterized in that the secondary mode signal is 1 if

the peripheral butterfly is used for the last transformation step, and otherwise 0.

13. The calculation method according to claim 12, characterized in that, for four input samples e1, e2, e3, and e4, and for a complex coefficient $W^s=A+j.B$, the butterfly delivers the following output samples s1, s2, s3, and s4

1) if the primary mode and secondary mode signals are 0: s1 = e1 + e2

s2 = e1 − e2

s3 = e4 − e3

s4 = e3 + e4

2) if the primary mode signal is 0 and the secondary mode signal is 1:

s1 = e1 + e2 + e3 + e4

s2 = e1 − e2

s3 = e4 − e3

s4 = (e1 + e2) − (e3 + e4)

3) if the primary mode signal is 1 and the permutation signal is 0:

s1 = e1 + A.e3 − B.e4

s2 = e2 + B.e3 + A.e4

s3 = e1 − A.e3 + B.e4

s4 = -e2 + B.e3 + A.e4

4) if the primary mode signal is 1 and the permutation signal is 1:

s1 = e1 − A.e3 + B.e4

s2 = -e2 + B.e3 + A.e4

s3 = e1 − A.e3 − B.e4

s4 = e2 + B.e3 + A.e4

14. The calculation method according to claim 10, characterized in that, in transformation step E_p, each calculation block comprises:

- 2^p-1 internal butterflies and a peripheral butterfly for the even values of index p as well as for the last transformation step if μ is even, and
- 2^p-1 internal butterflies, otherwise.

15. The calculation method according to claim 13, characterized in that the secondary mode signal is 1 if the peripheral butterfly is used for the last transformation step with μ being odd, and otherwise 0.

16. The calculation method according to claim 15, characterized in that, for four input samples e1, e2, e3, and e4, and for a complex coefficient $W^s=A+j.B$, the butterfly delivers the following output samples s1, s2, s3, and s4

1) if primary mode, secondary mode and permutation signals are 0:

s1 = e1 + e2 + e3 + e4
s2 = e1 − e2
s3 = e4 − e3
s4 = (e1 + e2) − (e3 + e4)

2) if the primary mode signal is 0 and the secondary mode signal is 1:

s1 = e1 + e4
s2 = e2
s3 = e3
s4 = e1 − e4

3) if the primary mode signal is 0 and the permutation signal is 1:

s1 = (e3 + e4) − (e1 + e2)

$s2 = e1 - e2$

$s3 = e4 - e3$

$s4 = e1 + e2 + e3 + e4$

4) if the primary mode signal is 1 and the permutation signal is 0:

$s1 = e1 + A.e3 - B.e4$

$s2 = e2 + B.e3 + A.e4$

$s3 = e1 - A.e3 + B.e4$

$s4 = -e2 + B.e3 + A.e4$

5) if the primary mode signal is 1 and the permutation signal is 1:

$s1 = e1 - A.e3 + B.e4$

$s2 = -e2 + B.e3 + A.e4$

$s3 = e1 + A.e3 - B.e4$

$s4 = e2 + B.e3 + A.e4$

17. The calculation method according to claim 10, characterized in that, in transformation step E_p, each calculation block comprises:

- 2^p-1 internal butterflies and a peripheral butterfly for the even values of index p, and
- 2^p-1 internal butterflies, otherwise.

18. The calculation method according to claim 17, characterized in that the secondary mode signal is 1 if the peripheral butterfly is used for the first transformation step with μ being even, and otherwise 0.

19. The calculation method according to claim 18, characterized in that, for four input samples e1, e2, e3, and e4, and for a complex coefficient $W^s=A+j.B$, the butterfly delivers the following output samples s1, s2, s3, and s4

1) if the primary mode signal is 0 and the secondary mode signal is 1:

$$s1 = e1 + e2$$
$$s2 = e1 - e2$$
$$s3 = e4 - e3$$
$$s4 = e3 + e4$$

2) if primary mode, secondary mode and permutation signals are 0:

$$s1 = e1 + e2 + e3 + e4$$
$$s2 = e1 - e2$$
$$s3 = e4 - e3$$
$$s4 = (e1 + e2) - (e3 + e4)$$

3) if the primary mode and secondary mode signals are 0 and the permutation signal is 1:

$$s1 = (e3 + e4) - (e1 + e2)$$
$$s2 = e1 - e2$$
$$s3 = e4 - e3$$
$$s4 = e1 + e2 + e3 + e4$$

4) if the primary mode signal is 1 and the permutation signal is 0:

$$s1 = e1 + A.e3 - B.e4$$
$$s2 = e2 + B.e3 + A.e4$$
$$s3 = e1 - A.e3 + B.e4$$
$$s4 = -e2 + B.e3 + A.e4$$

5) if the primary mode signal is 1 and the permutation signal is 1:

$$s1 = e1 - A.e3 + B.e4$$
$$s2 = -e2 + B.e3 + A.e4$$
$$s3 = e1 + A.e3 - B.e4$$
$$s4 = e2 + B.e3 + A.e4$$

20. The calculation method according to claim 8, characterized in that the butterflies for implementing the transformation steps are all of the same type and have

- four inputs for receiving input samples and four outputs for providing output samples,

- four additional inputs, respectively primary mode, secondary mode, permutation, and coefficient inputs,

in order to selectively apply different transformation operations to the input samples, each operation being determined by the values assigned to the primary mode, secondary mode, permutation signals, and a coefficient admitted on said corresponding additional inputs,

and in that the final step furthermore performs an addition and subtraction between the first and the last output sample provided in the last transformation step.

21. The calculation method according to claim 20, characterized in that, in transformation step E_p, each calculation block comprises one peripheral butterfly and 2^p-1 internal butterflies.

22. The calculation method according to claim 21, characterized in that, for four input samples e1, e2, e3, and e4, and for a complex coefficient $W^s=A+j.B$, the butterfly delivers the following output samples s1, s2, s3, and s4

1) if the primary mode signal is 0:

s1 = e1 + e2

s2 = e1 - e2

s3 = e4 - e3

s4 = e3 + e4

2) if the primary mode signal is 1 and the permutation signal is 0:

s1 = e1 + A.e3 − B.e4

s2 = e2 + B.e3 + A.e4

s3 = e1 − A.e3 + B.e4

s4 = −e2 + B.e3 + A.e4

3) if the primary signal is 1 and the permutation signal is 1:

s1 = e1 − A.e3 + B.e4

s2 = −e2 + B.e3 + A.e4

s3 = e1 + A.e3 −B.e4

s4 = e2 + B.e3 + A.e4

23. The calculation method according to claim 9 or 20, characterized in that the first and second binary addresses of μ bits are generated for each butterfly, each binary address corresponding to the rank of an input sample of said butterfly and the second binary address being greater than the first binary address.

24. The calculation method according to claim 23, characterized in that said first and second binary addresses are consecutive and an internal butterfly is involved.

25. The calculation method according to claim 23 or 24, characterized in that, if a peripheral butterfly is involved, the p+2 low-order bits of the first address are equal to 0, and the p+2 low-order bits of the second address form a number equal to $2^{p+1}-1$.

26. The calculation method according to claim 24 or 25, characterized in that the address of the two other samples to be applied to the inputs of the

butterfly, be they peripheral or internal, are obtained by inverting the (p+2) low-order bits of said first and second produced addresses.

27. The calculation method according to claim 26, characterized in that even-numbered address samples and odd-numbered address samples are stored in two separate memories.

28. The calculation method according to claim 25, characterized in that the value of the parameter s of the coefficient W^s assigned to an internal butterfly in transformation step E_p is coded by μ-2 bits, and is:

- if p+1=μ-2, the number formed by the p+1 low-order bits of the second binary address produced for said internal butterfly,
- if p+1<μ-2, the number formed by the p+1 low-order bits of the second binary address produced for said internal butterfly, followed by μ-p-3 zero bits at the end of the number,
- if p+1>μ-2, the number formed by the p+1 low-order bits of the second binary address produced for said internal butterfly, minus its μ-p-1 low-order bits.

29. The calculation method according to claim 4, in turn dependent on claim 3, in turn dependent on claim 2, characterized in that in each transformation step E_p, the butterflies are distributed among 2^p calculation blocks,

in that each calculation block comprises one peripheral butterfly and $N/2^{p+2}-1$ internal butterflies,

in that the peripheral butterfly of the rank α calculation block in transformation step $E_β$ transforms

the input samples of rank $2^{\mu-\beta}\alpha$, $2^{\mu-\beta}\alpha+2^{\mu-\beta-1}-1$, $2^{\mu-\beta}\alpha+2^{\mu-\beta-1}$, $2^{\mu-\beta}\alpha+2^{\mu-\beta}-1$ into output samples of the same rank,

and in that the internal rank τ butterfly of the rank α calculation block in transformation step E_β transforms the input samples of rank $2^{\mu-\beta}\alpha+2\tau+1$, $2^{\mu-\beta}\alpha+2\tau+2$, $2^{\mu-\beta}\alpha+2^{\mu-\beta-1}-2\tau-3$, $2^{\mu-\beta}\alpha+2^{\mu-\beta}-2\tau-2$ into output samples of the same rank.

30. The calculation method according to claim 29, characterized in further comprising a final step of modifying the sequence of the output samples provided in the last transformation step and classifying them in ascending order of index n.

31. The calculation method according to claim 29 or 30, characterized in that each butterfly is assigned a coefficient W^s, whereon the calculation inside the butterfly is based, said coefficient being equal to $e^{-j(2\pi s/N)}$ with $s \in [0..N/4-1]$ for a fast Fourier transform and is equal to $e^{j(2\pi s/N)}$ with $s \in [0..N/4-1]$ for an inverse fast Fourier transform.

32. Calculation method according to claim 31, characterized in that the internal rank τ butterfly of the rank α calculation block in transformation step E_β is assigned coefficient W^δ with $\delta = (\tau+1).2^\beta$.

33. The calculation method according to claim 32, characterized in that the butterflies for implementing the transformation steps are all of the same type and have

- four inputs for receiving input samples and four outputs for providing output samples,

- four additional inputs, respectively primary mode, secondary mode, permutation, and coefficient inputs,

in order to selectively apply different transformation operations to the input samples, each operation being determined by the values assigned to the primary mode, secondary mode, permutation signals, and a coefficient admitted on said corresponding additional inputs.

34. The calculation method according to claim 33, characterized in that, for each butterfly, the primary mode signal is 0 for a peripheral butterfly and 1 for an internal butterfly,

in that the permutation signal is 0 for the even rank calculation blocks, including rank 0, and 1 for the odd values.

35. The calculation method according to claim 31 or 34, characterized in that the secondary mode signal is 1 if the butterfly, be it peripheral or internal, is used for the first transformation step, and otherwise 0.

36. The calculation method according to claim 35, characterized in that, for four input samples e1, e2, e3, and e4, and for a complex coefficient $W^s = A + j.B$, the butterfly delivers the following output samples s1, s2, s3, and s4

1) if the primary mode and secondary mode signals are 0:

$$s1 = (e1 + e2)/2$$
$$s2 = (e1 - e2)/2$$
$$s3 = (e4 - e3)/2$$

s4 = (e3 + e4)/2

2) if the primary mode signal is 0 and the secondary mode signal is 1:

s1 = [(e1+e4)/2-e2]/2

s2 = [(e1+e4)/2-e2]/2

s3 = [e3-(e1-e4)/2]/2

s4 = [e3+(e1+e4)/2]/2

3) if the primary mode signal is 1 and the permutation signal is 0:

s1 = (e1+e3)/2

s2 = (e2+e4)/2

s3 = [(e1-e3).A - (e2+e4).B]/2

s4 = [-(e1-e3).B + (e2+e4).A]/2

4) if the primary mode signal is 1 and the permutation signal is 1:

s1 = [(e1-e3).A - (e2+e4).B]/2

s2 = [-(e1-e3).B + (e2+e4).A]/2

s3 = (e1+e3)/2

s4 = (e2-e4)/2

37. The calculation method according to claim 33, characterized in that the first and second binary addresses of μ bits are generated for each butterfly, each binary address corresponding to the rank of an input sample of said butterfly and the second binary address being greater than the first binary address.

38. The calculation method according to claim 37, characterized in that said first and second binary addresses are consecutive and an internal butterfly is involved.

39. The calculation method according to claim 37 or 38, characterized in that, if a peripheral butterfly

is involved, the μ-p low-order bits of the first address are equal to 0, and the μ-p low-order bits of the second address form a number equal to $N/2^{p+1}-1$.

40. The calculation method according to claim 38 or 39, characterized in that the address of the two other samples to be applied to the inputs of the butterfly are obtained by inverting the μ-p low-order bits of both produced addresses.

41. The calculation method according to claim 40, characterized in that even-numbered address samples and odd-numbered address samples are stored in two separate memories.

42. The calculation method according to claim 41, characterized in that the value of the parameter s of the coefficient W^s assigned to an internal butterfly in transformation step E_p is coded by μ-2 bits, and is:

- if $\mu-p-1=\mu-2$, the number formed by the μ-p-1 low-order bits of the second address produced for said internal butterfly,
- if $\mu-p-1<\mu-2$, the number formed by the μ-p-1 low-order bits of the second address produced for said internal butterfly, followed by p-1 zero bits at the end of the number,
- if $\mu-p-1>\mu-2$, the number formed by the μ-p-1 low-order bits of the second address produced for said internal butterfly, minus its p+1 low-order bits.

ABSTRACT

METHOD OF CALCULATING THE FAST FOURIER TRANSFORM AND THE INVERSE FAST FOURIER TRANSFORM

The invention relates to a method of calculating the fast Fourier transform or the inverse fast Fourier transform of a series of N real samples x(n), with N power of two, operating according to a time interleaving algorithm and providing the sample series X(n) in ascending order of index n and using limited calculation and storage means. The invention also relates to a method of calculating the fast Fourier transform or the inverse fast Fourier transform of a series of N conjugated complex samples X(n), with N power of two, operating according to a frequency interleaving algorithm.

Fig. 8

Application: image or acoustic signal processing, multicarrier modulation.

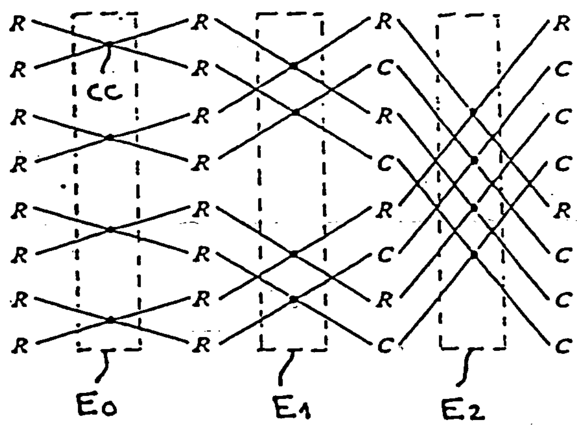


FIG.1A

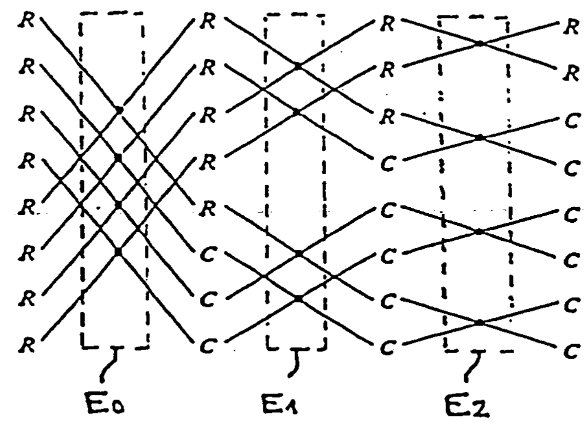


FIG.1B

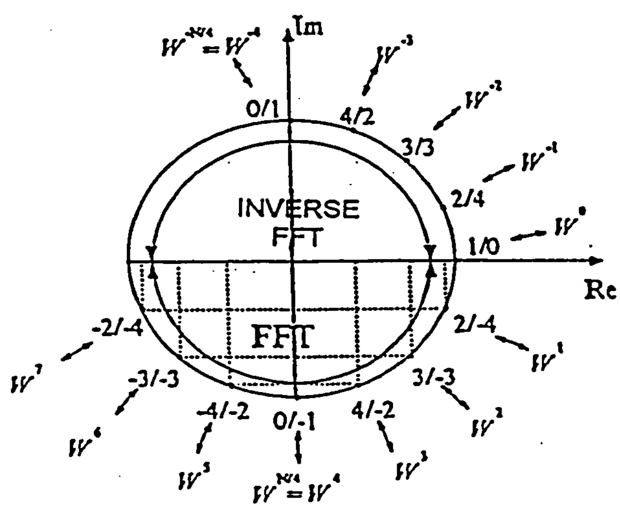


FIG. 3

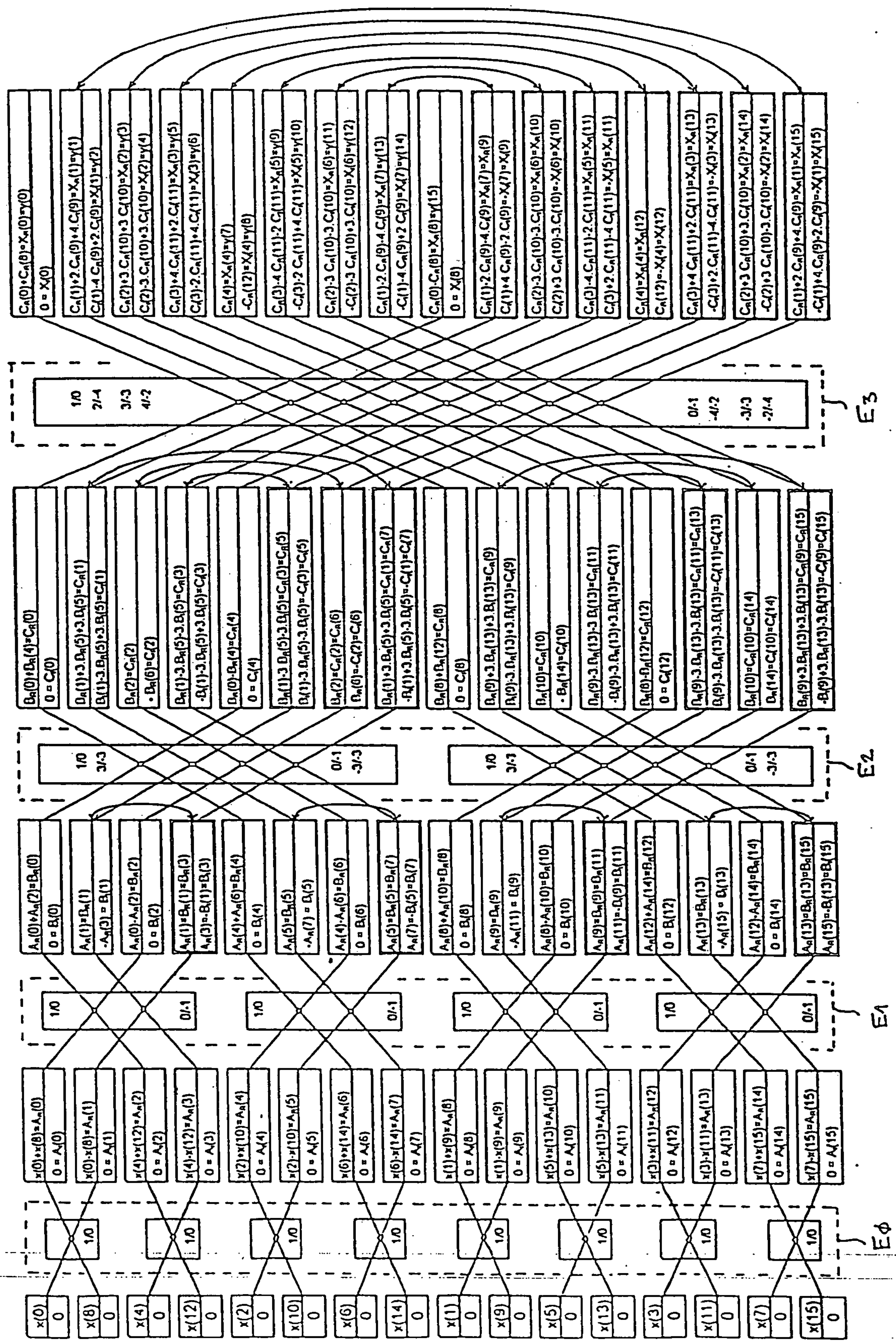


FIG.2

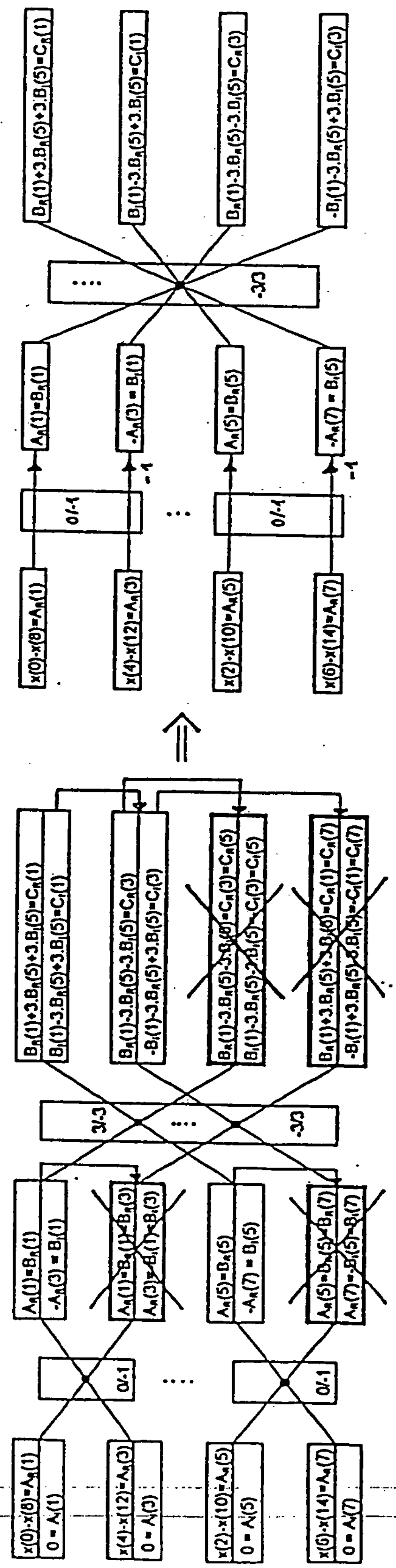
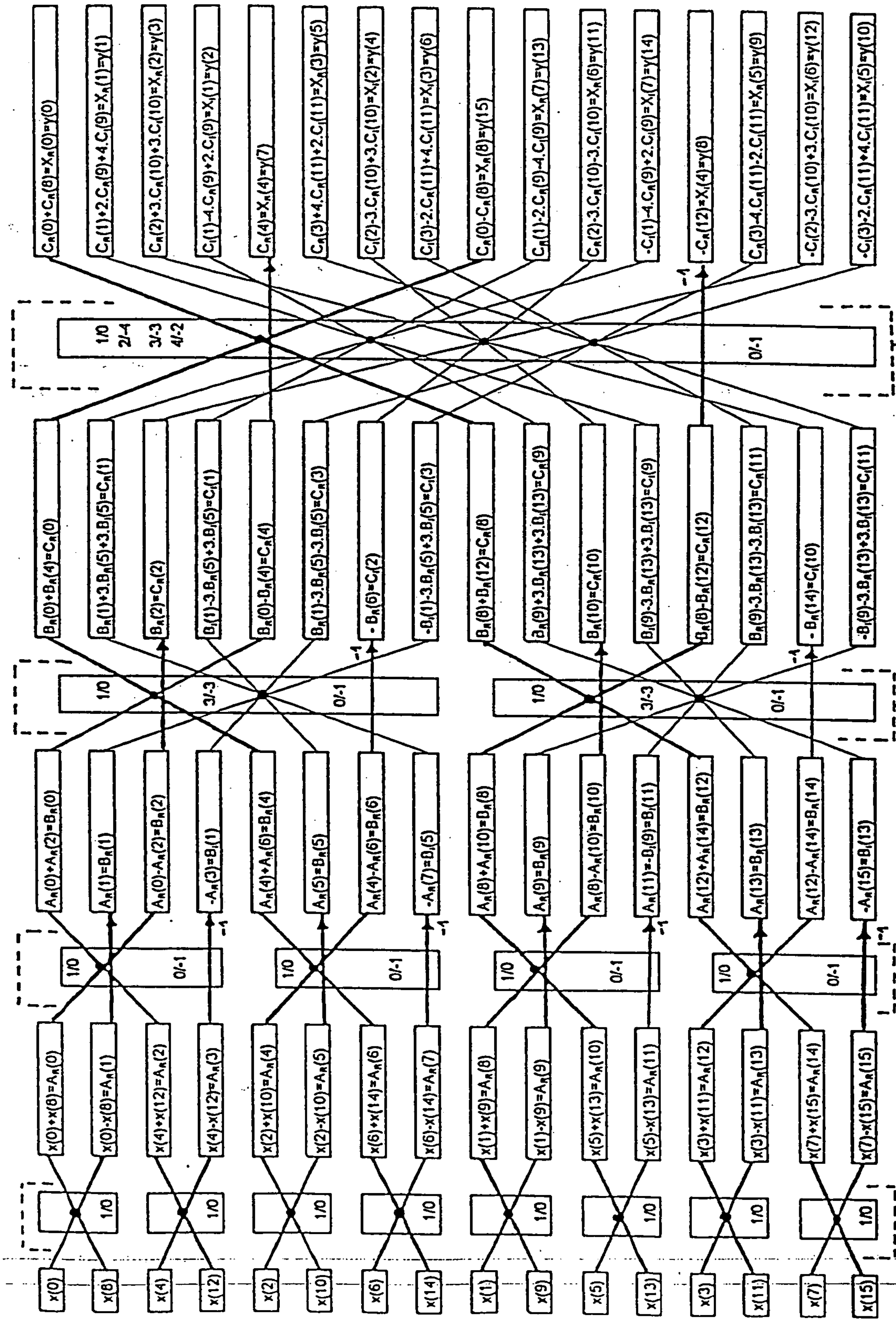

$x(0)-x(8)=A_R(1)$
$0 = A_I(1)$
$x(4)-x(12)=A_R(3)$
$0 = A_I(3)$
$x(2)-x(10)=A_R(5)$
$0 = A_I(5)$
$x(6)-x(14)=A_R(7)$
$0 = A_I(7)$
0/-1
....
0/-1
$A_R(1)=B_R(1)$
$-A_R(3) = B_I(1)$
$A_R(5)=B_R(5)$
$-A_R(7) = B_I(5)$
3/-3
....
-3/3
$B_R(1)+3.B_R(5)+3.B_I(5)=C_R(1)$
$B_I(1)-3.B_R(5)+3.B_I(5)=C_I(1)$
$B_R(1)-3.B_R(5)-3.B_I(5)=C_R(3)$
$-B_I(1)-3.B_R(5)+3.B_I(5)=C_I(3)$
$x(0)-x(8)=A_R(1)$
$x(4)-x(12)=A_R(3)$
$x(2)-x(10)=A_R(5)$
$x(6)-x(14)=A_R(7)$
0/-1
0/-1
-1
-1
$A_R(1)=B_R(1)$
$-A_R(3) = B_I(1)$
$A_R(5)=B_R(5)$
$-A_R(7) = B_I(5)$
....
-3/3
$B_R(1)+3.B_R(5)+3.B_I(5)=C_R(1)$
$B_I(1)-3.B_R(5)+3.B_I(5)=C_I(1)$
$B_R(1)-3.B_R(5)-3.B_I(5)=C_R(3)$
$-B_I(1)-3.B_R(5)+3.B_I(5)=C_I(3)$

FIG. 4



x(0)
x(8)
x(4)
x(12)
x(2)
x(10)
x(6)
x(14)
x(1)
x(9)
x(5)
x(13)
x(3)
x(11)
x(7)
x(15)
E_0
1/0
$x(0)+x(8)=A_R(0)$
$x(0)-x(8)=A_R(1)$
$x(4)+x(12)=A_R(2)$
$x(4)-x(12)=A_R(3)$
$x(2)+x(10)=A_R(4)$
$x(2)-x(10)=A_R(5)$
$x(6)+x(14)=A_R(6)$
$x(6)-x(14)=A_R(7)$
$x(1)+x(9)=A_R(8)$
$x(1)-x(9)=A_R(9)$
$x(5)+x(13)=A_R(10)$
$x(5)-x(13)=A_R(11)$
$x(3)+x(11)=A_R(12)$
$x(3)-x(11)=A_R(13)$
$x(7)+x(15)=A_R(14)$
$x(7)-x(15)=A_R(15)$
E_1
1/0
0/-1
-1
$A_R(0)+A_R(2)=B_R(0)$
$A_R(1)=B_R(1)$
$A_R(0)-A_R(2)=B_R(2)$
$-A_R(3)=B_I(1)$
$A_R(4)+A_R(6)=B_R(4)$
$A_R(5)=B_R(5)$
$A_R(4)-A_R(6)=B_R(6)$
$-A_R(7)=B_I(5)$
$A_R(8)+A_R(10)=B_R(8)$
$A_R(9)=B_R(9)$
$A_R(8)-A_R(10)=B_R(10)$
$A_R(11)=-B_I(9)=B_I(11)$
$A_R(12)+A_R(14)=B_R(12)$
$A_R(13)=B_R(13)$
$A_R(12)-A_R(14)=B_R(14)$
$-A_R(15)=B_I(13)$
E_2
1/0
3/-3
0/-1
-1
$B_R(0)+B_R(4)=C_R(0)$
$B_R(1)+3.B_R(5)+3.B_I(5)=C_R(1)$
$B_R(2)=C_R(2)$
$B_I(1)-3.B_R(5)+3.B_I(5)=C_I(1)$
$B_R(0)-B_R(4)=C_R(4)$
$B_R(1)-3.B_R(5)-3.B_I(5)=C_R(3)$
$-B_R(6)=C_I(2)$
$-B_I(1)-3.B_R(5)+3.B_I(5)=C_I(3)$
$B_R(8)+B_R(12)=C_R(8)$
$B_R(9)+3.B_R(13)+3.B_I(13)=C_R(9)$
$B_R(10)=C_R(10)$
$B_I(9)-3.B_R(13)+3.B_I(13)=C_I(9)$
$B_R(8)-B_R(12)=C_R(12)$
$B_R(9)-3.B_R(13)-3.B_I(13)=C_R(11)$
$-B_R(14)=C_I(10)$
$-B_I(9)-3.B_R(13)+3.B_I(13)=C_I(11)$
E_3
1/0
2/-4
3/-3
4/-2
0/-1
-1
$C_R(0)+C_R(8)=X_R(0)=y(0)$
$C_R(1)+2.C_R(9)+4.C_I(9)=X_R(1)=y(1)$
$C_R(2)+3.C_R(10)+3.C_I(10)=X_R(2)=y(3)$
$C_I(1)-4.C_R(9)+2.C_I(9)=X_I(1)=y(2)$
$C_R(4)=X_R(4)=y(7)$
$C_R(3)+4.C_R(11)+2.C_I(11)=X_R(3)=y(5)$
$C_I(2)-3.C_R(10)+3.C_I(10)=X_I(2)=y(4)$
$C_I(3)-2.C_R(11)+4.C_I(11)=X_I(3)=y(6)$
$C_R(0)-C_R(8)=X_R(8)=y(15)$
$C_R(1)-2.C_R(9)-4.C_I(9)=X_R(7)=y(13)$
$C_R(2)-3.C_R(10)-3.C_I(10)=X_R(6)=y(11)$
$-C_I(1)-4.C_R(9)+2.C_I(9)=X_I(7)=y(14)$
$-C_R(12)=X_I(4)=y(8)$
$C_R(3)-4.C_R(11)-2.C_I(11)=X_R(5)=y(9)$
$-C_I(2)-3.C_R(10)+3.C_I(10)=X_I(6)=y(12)$
$-C_I(3)-2.C_R(11)+4.C_I(11)=X_I(5)=y(10)$

FIG. 5

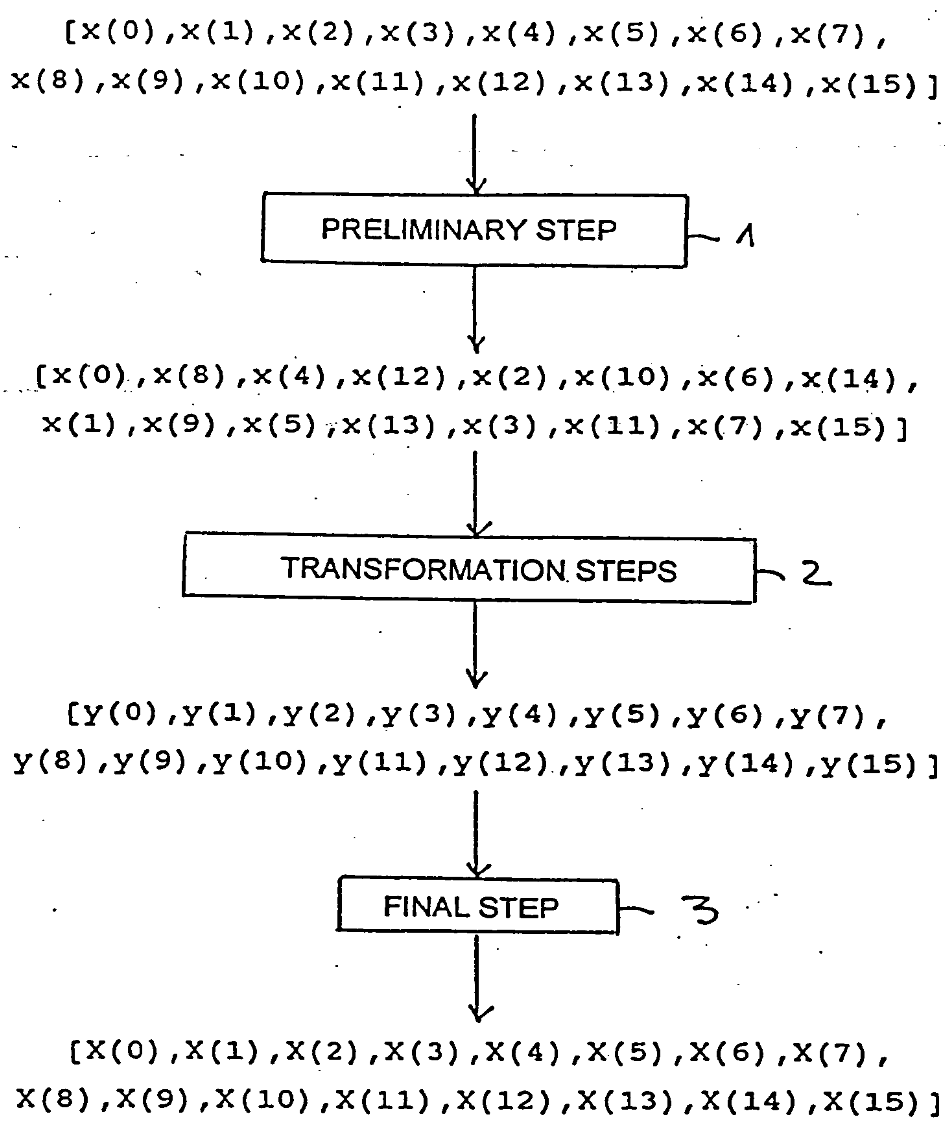

[x(0),x(1),x(2),x(3),x(4),x(5),x(6),x(7),
x(8),x(9),x(10),x(11),x(12),x(13),x(14),x(15)]
PRELIMINARY STEP
1
[x(0),x(8),x(4),x(12),x(2),x(10),x(6),x(14),
x(1),x(9),x(5),x(13),x(3),x(11),x(7),x(15)]
TRANSFORMATION STEPS
2
[y(0),y(1),y(2),y(3),y(4),y(5),y(6),y(7),
y(8),y(9),y(10),y(11),y(12),y(13),y(14),y(15)]
FINAL STEP
3
[X(0),X(1),X(2),X(3),X(4),X(5),X(6),X(7),
X(8),X(9),X(10),X(11),X(12),X(13),X(14),X(15)]

FIG. 6

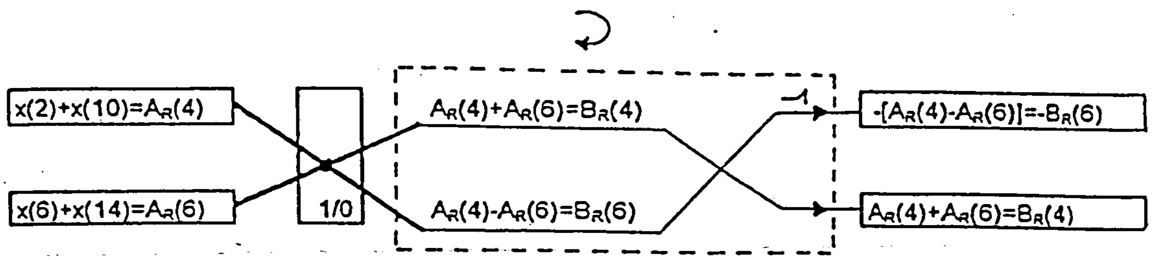


FIG.7A

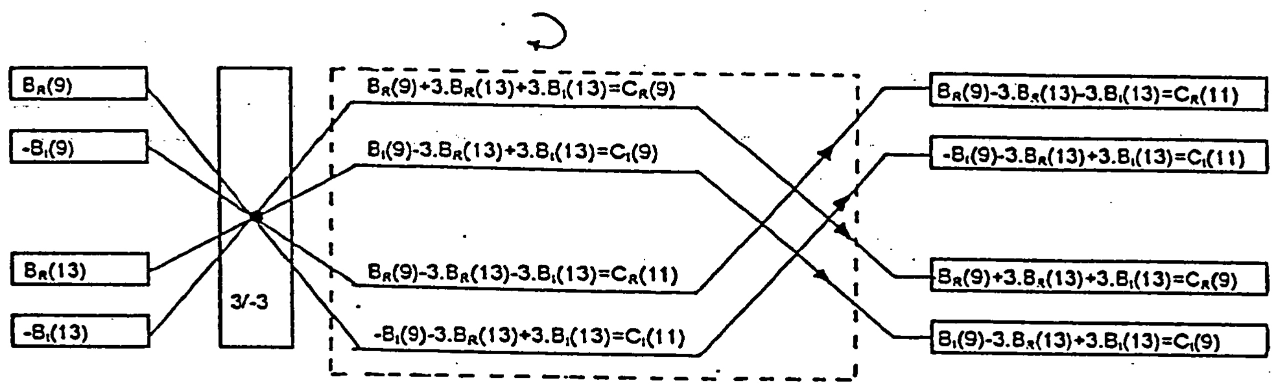


FIG.7B

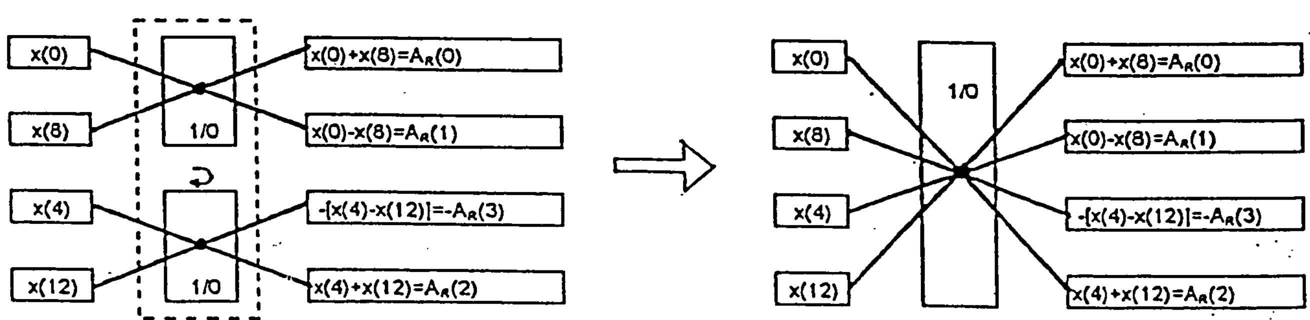


FIG.9

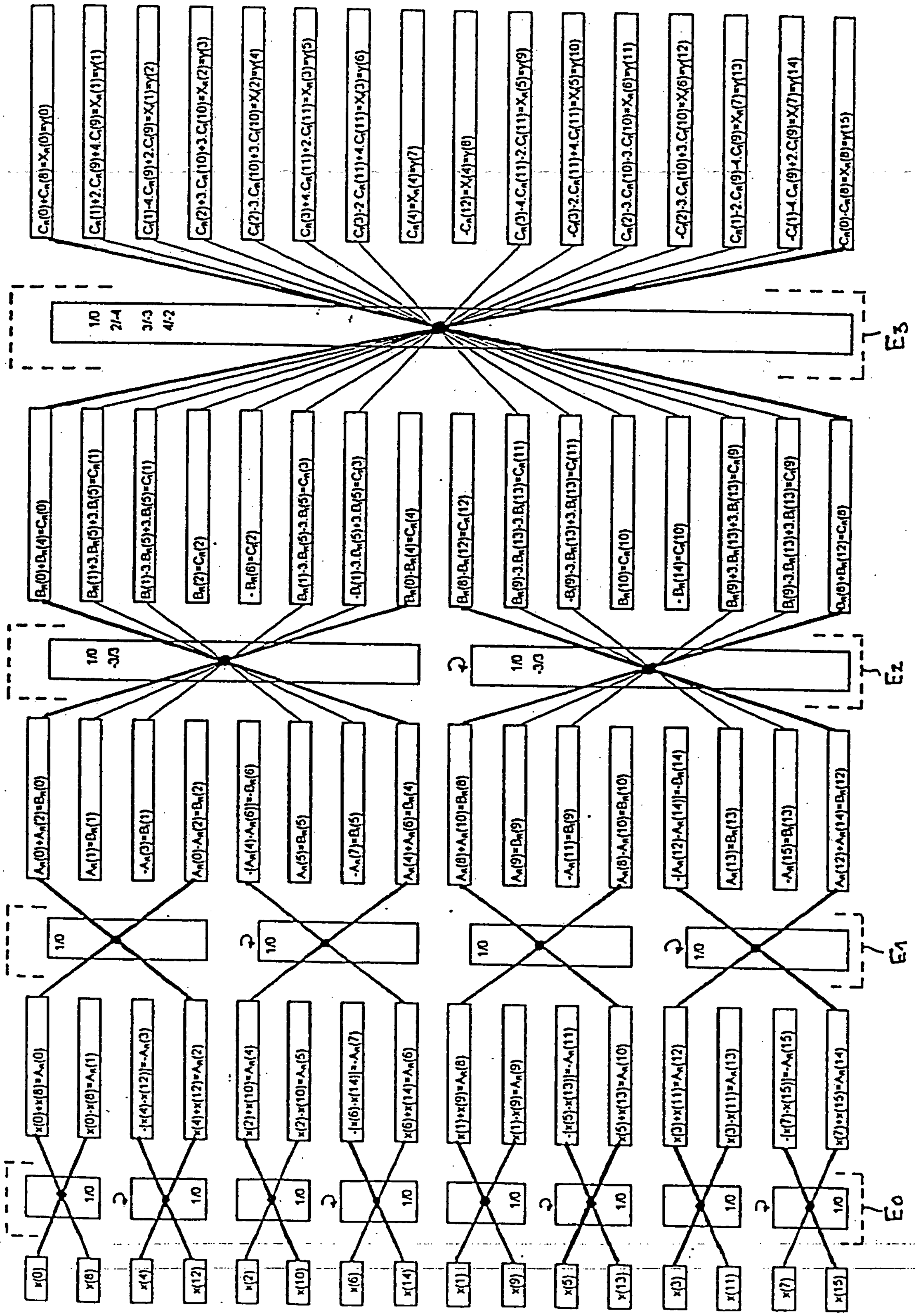


FIG. 8

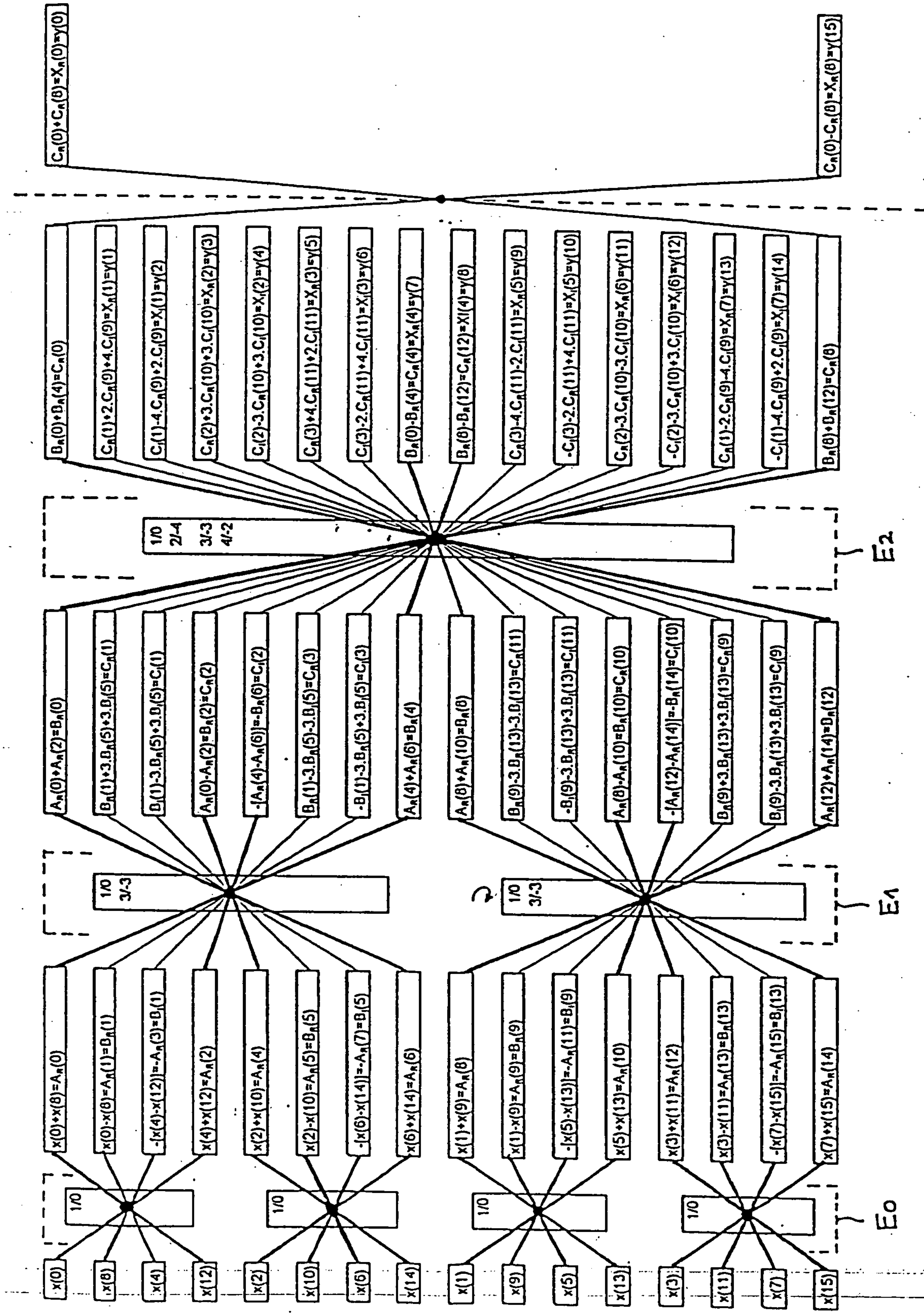

x(0)
x(8)
x(4)
x(12)
x(2)
x(10)
x(6)
x(14)
x(1)
x(9)
x(5)
x(13)
x(3)
x(11)
x(7)
x(15)
1/0
E0
x(0)+x(8)=A_R(0)
x(0)-x(8)=A_R(1)=B_R(1)
-I(x(4)-x(12))=-A_R(3)=B_I(1)
x(4)+x(12)=A_R(2)
x(2)+x(10)=A_R(4)
x(2)-x(10)=A_R(5)=B_R(5)
-I(x(6)-x(14))=-A_R(7)=B_I(5)
x(6)+x(14)=A_R(6)
x(1)+x(9)=A_R(8)
x(1)-x(9)=A_R(9)=B_R(9)
-I(x(5)-x(13))=-A_R(11)=B_I(9)
x(5)+x(13)=A_R(10)
x(3)+x(11)=A_R(12)
x(3)-x(11)=A_R(13)=B_R(13)
-I(x(7)-x(15))=-A_R(15)=B_I(13)
x(7)+x(15)=A_R(14)
1/0
3/-3
E1
A_R(0)+A_R(2)=B_R(0)
B_R(1)+3.B_R(5)+3.B_I(5)=C_R(1)
B_I(1)-3.B_R(5)+3.B_I(5)=C_I(1)
A_R(0)-A_R(2)=B_R(2)=C_R(2)
-I(A_R(4)-A_R(6))=-B_R(6)=C_I(2)
B_R(1)-3.B_R(5)-3.B_I(5)=C_R(3)
-B_I(1)-3.B_R(5)+3.B_I(5)=C_I(3)
A_R(4)+A_R(6)=B_R(4)
A_R(8)+A_R(10)=B_R(8)
B_R(9)-3.B_R(13)-3.B_I(13)=C_R(11)
-B_I(9)-3.B_R(13)+3.B_I(13)=C_I(11)
A_R(8)-A_R(10)=B_R(10)=C_R(10)
-I(A_R(12)-A_R(14))=-B_R(14)=C_I(10)
B_R(9)+3.B_R(13)+3.B_I(13)=C_R(9)
B_I(9)-3.B_R(13)+3.B_I(13)=C_I(9)
A_R(12)+A_R(14)=B_R(12)
1/0
2/-4
3/-3
4/-2
E2
B_R(0)+B_R(4)=C_R(0)
C_R(1)+2.C_R(9)+4.C_I(9)=X_R(1)=y(1)
C_I(1)-4.C_R(9)+2.C_I(9)=X_I(1)=y(2)
C_R(2)+3.C_R(10)+3.C_I(10)=X_R(2)=y(3)
C_I(2)-3.C_R(10)+3.C_I(10)=X_I(2)=y(4)
C_R(3)+4.C_R(11)+2.C_I(11)=X_R(3)=y(5)
C_I(3)-2.C_R(11)+4.C_I(11)=X_I(3)=y(6)
B_R(0)-B_R(4)=C_R(4)=X_R(4)=y(7)
B_R(8)-B_R(12)=C_R(12)=X_I(4)=y(8)
C_R(3)-4.C_R(11)-2.C_I(11)=X_R(5)=y(9)
-C_I(3)-2.C_R(11)+4.C_I(11)=X_I(5)=y(10)
C_R(2)-3.C_R(10)-3.C_I(10)=X_R(6)=y(11)
-C_I(2)-3.C_R(10)+3.C_I(10)=X_I(6)=y(12)
C_R(1)-2.C_R(9)-4.C_I(9)=X_R(7)=y(13)
-C_I(1)-4.C_R(9)+2.C_I(9)=X_I(7)=y(14)
B_R(8)+B_R(12)=C_R(8)
C_R(0)+C_R(8)=X_R(0)=y(0)
C_R(0)-C_R(8)=X_R(8)=y(15)

FIG. 10

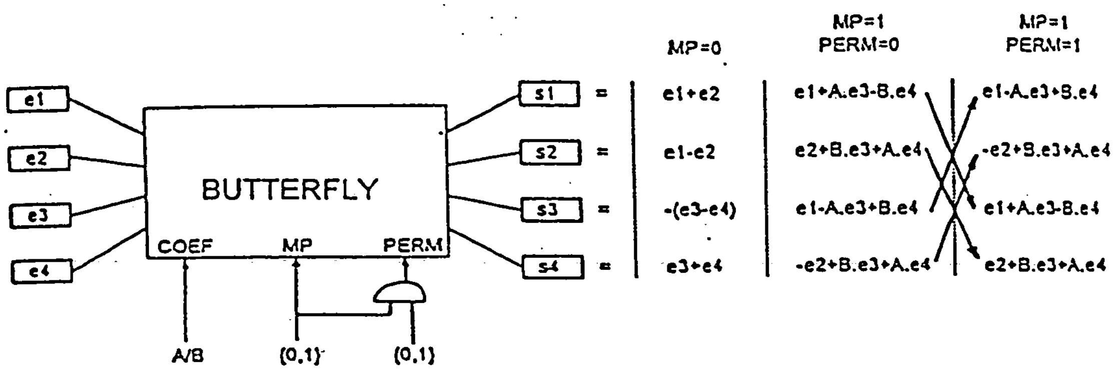

e1
e2
e3
e4
BUTTERFLY
COEF
MP
PERM
A/B
(0,1)
(0,1)
s1
s2
s3
s4
MP=0
MP=1
PERM=0
MP=1
PERM=1
e1+e2
e1-e2
-(e3-e4)
e3+e4
e1+A.e3-B.e4
e2+B.e3+A.e4
e1-A.e3+B.e4
-e2+B.e3+A.e4
e1-A.e3+B.e4
-e2+B.e3+A.e4
e1+A.e3-B.e4
e2+B.e3+A.e4

FIG.11

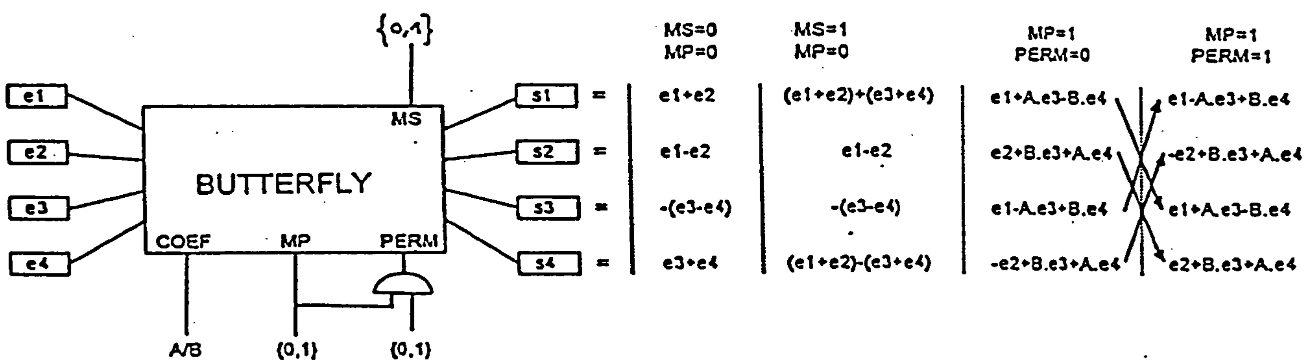

{0,1}
e1
e2
e3
e4
BUTTERFLY
MS
COEF
MP
PERM
A/B
(0,1)
(0,1)
s1
s2
s3
s4
MS=0
MP=0
MS=1
MP=0
MP=1
PERM=0
MP=1
PERM=1
e1+e2
e1-e2
-(e3-e4)
e3+e4
(e1+e2)+(e3+e4)
e1-e2
-(e3-e4)
(e1+e2)-(e3+e4)
e1+A.e3-B.e4
e2+B.e3+A.e4
e1-A.e3+B.e4
-e2+B.e3+A.e4
e1-A.e3+B.e4
-e2+B.e3+A.e4
e1+A.e3-B.e4
e2+B.e3+A.e4

FIG.13

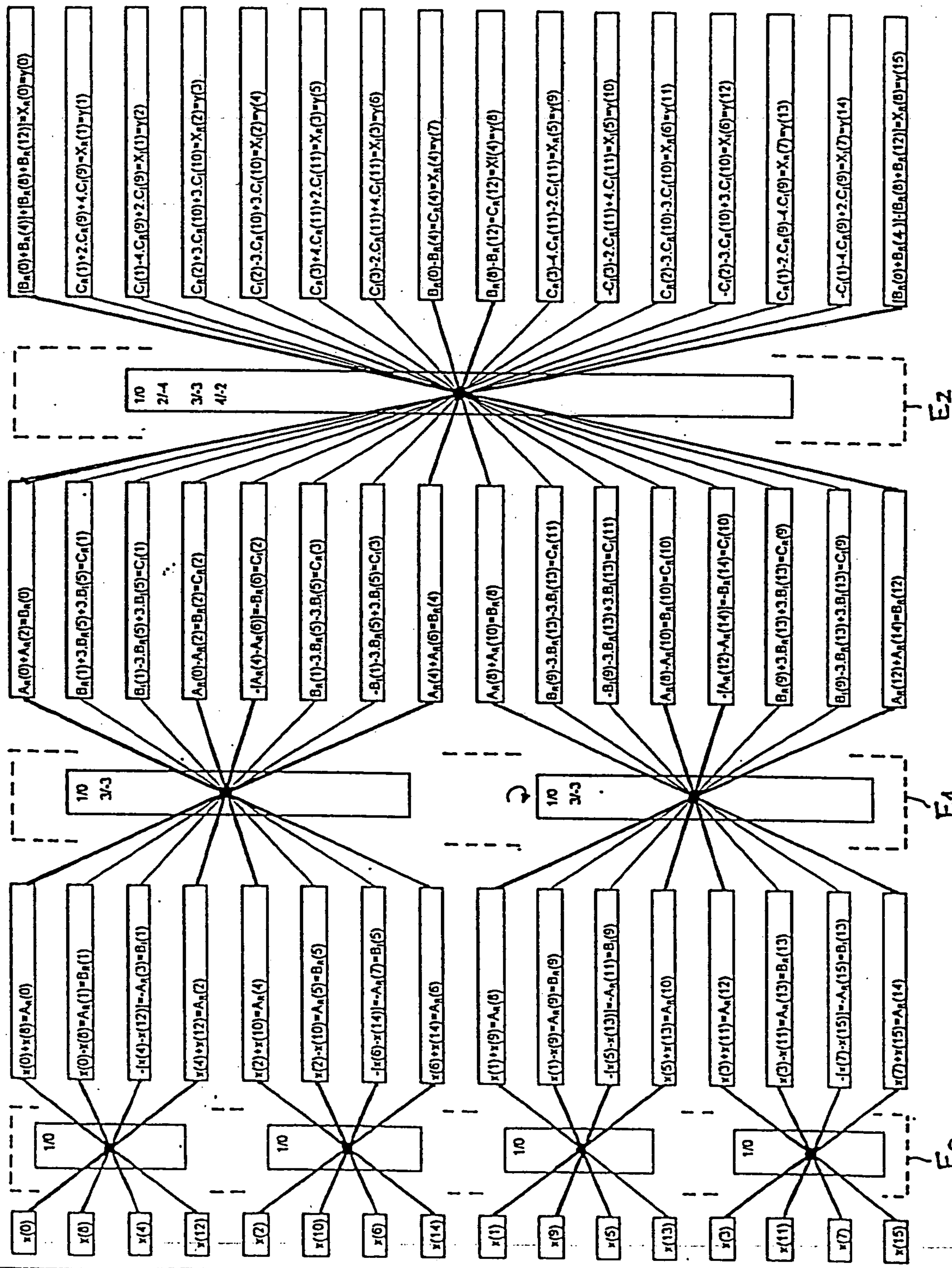


FIG.12

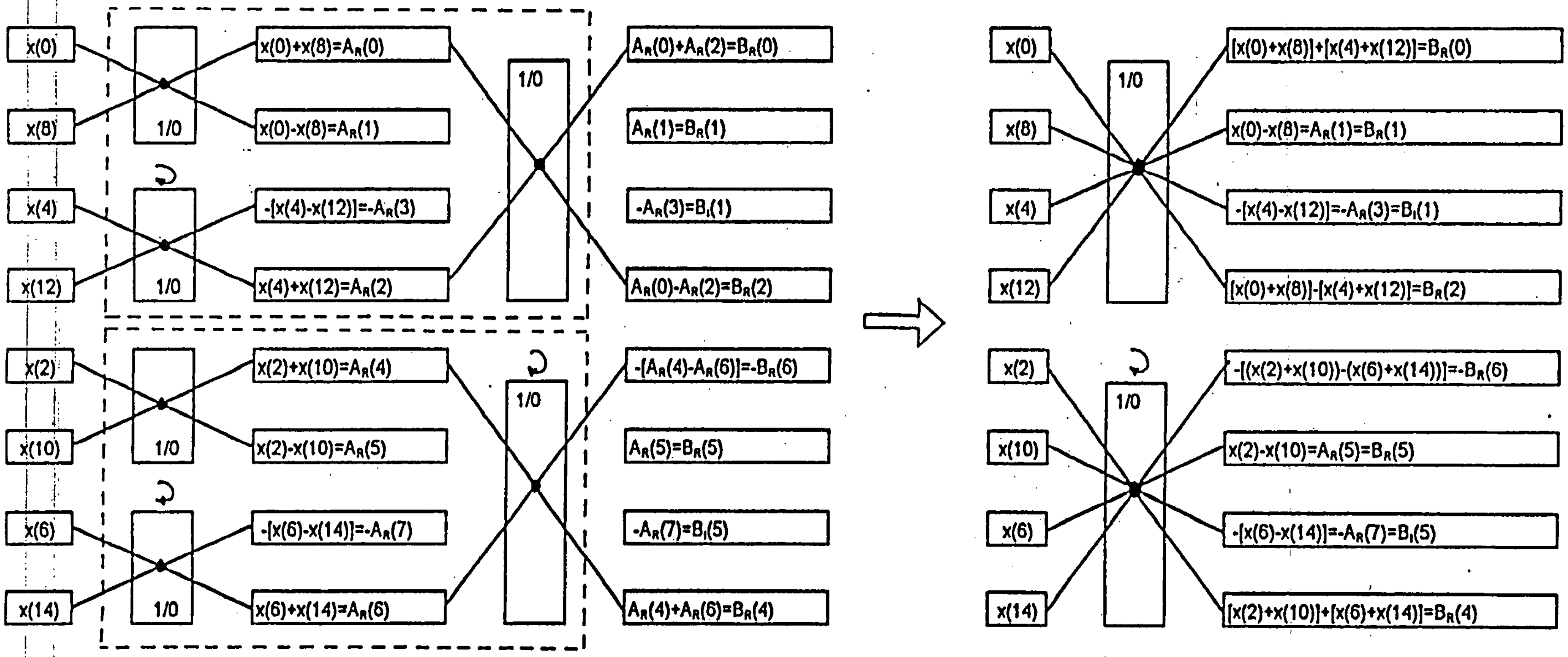

x(0)
x(8)
x(4)
x(12)
x(2)
x(10)
x(6)
x(14)
1/0
x(0)+x(8)=A_R(0)
x(0)-x(8)=A_R(1)
-[x(4)-x(12)]=-A_R(3)
x(4)+x(12)=A_R(2)
x(2)+x(10)=A_R(4)
x(2)-x(10)=A_R(5)
-[x(6)-x(14)]=-A_R(7)
x(6)+x(14)=A_R(6)
A_R(0)+A_R(2)=B_R(0)
A_R(1)=B_R(1)
-A_R(3)=B_I(1)
A_R(0)-A_R(2)=B_R(2)
-[A_R(4)-A_R(6)]=-B_R(6)
A_R(5)=B_R(5)
-A_R(7)=B_I(5)
A_R(4)+A_R(6)=B_R(4)
[x(0)+x(8)]+[x(4)+x(12)]=B_R(0)
x(0)-x(8)=A_R(1)=B_R(1)
-[x(4)-x(12)]=-A_R(3)=B_I(1)
[x(0)+x(8)]-[x(4)+x(12)]=B_R(2)
-[(x(2)+x(10))-(x(6)+x(14))]=-B_R(6)
x(2)-x(10)=A_R(5)=B_R(5)
-[x(6)-x(14)]=-A_R(7)=B_I(5)
[x(2)+x(10)]+[x(6)+x(14)]=B_R(4)

FIG. 14

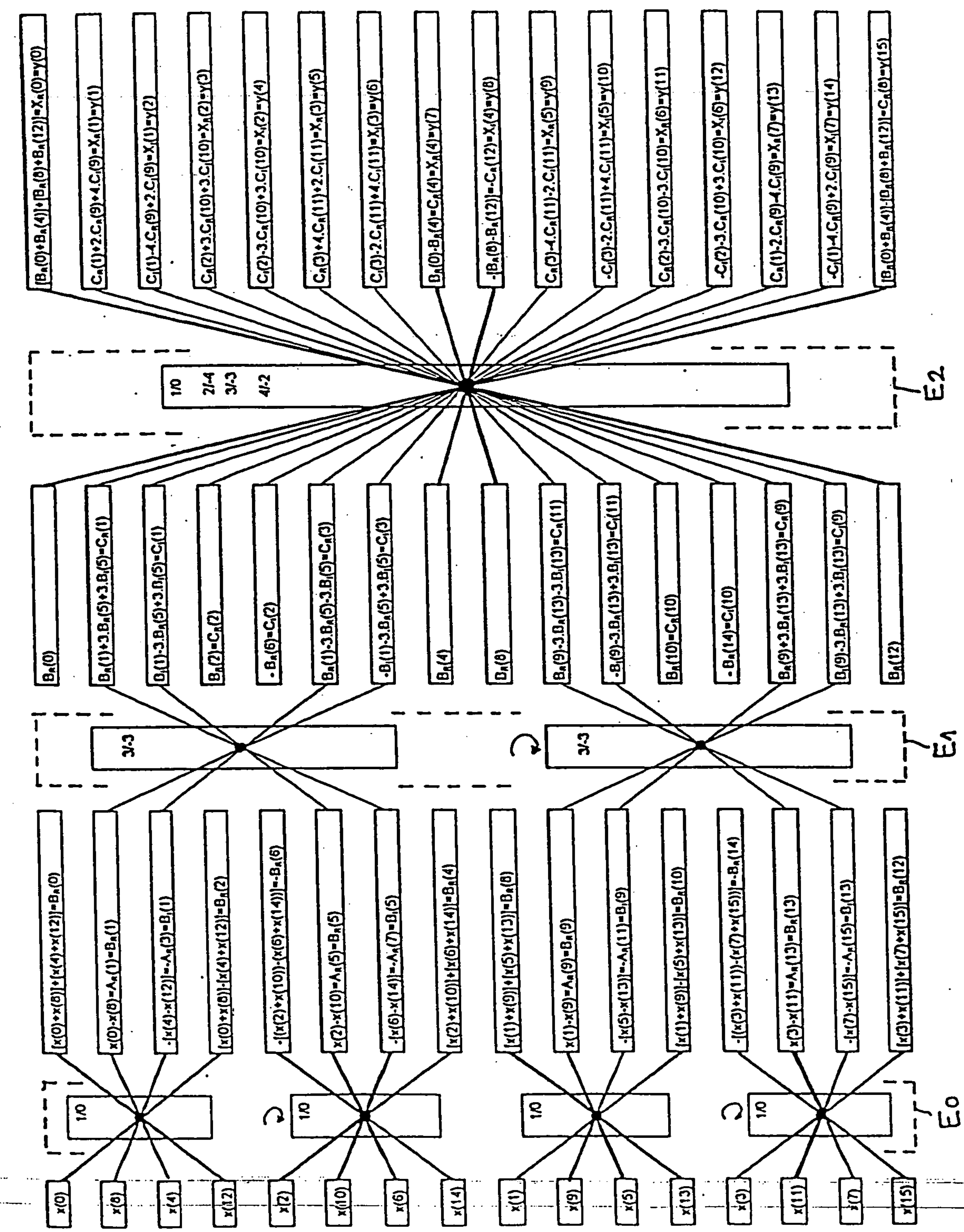


FIG.15

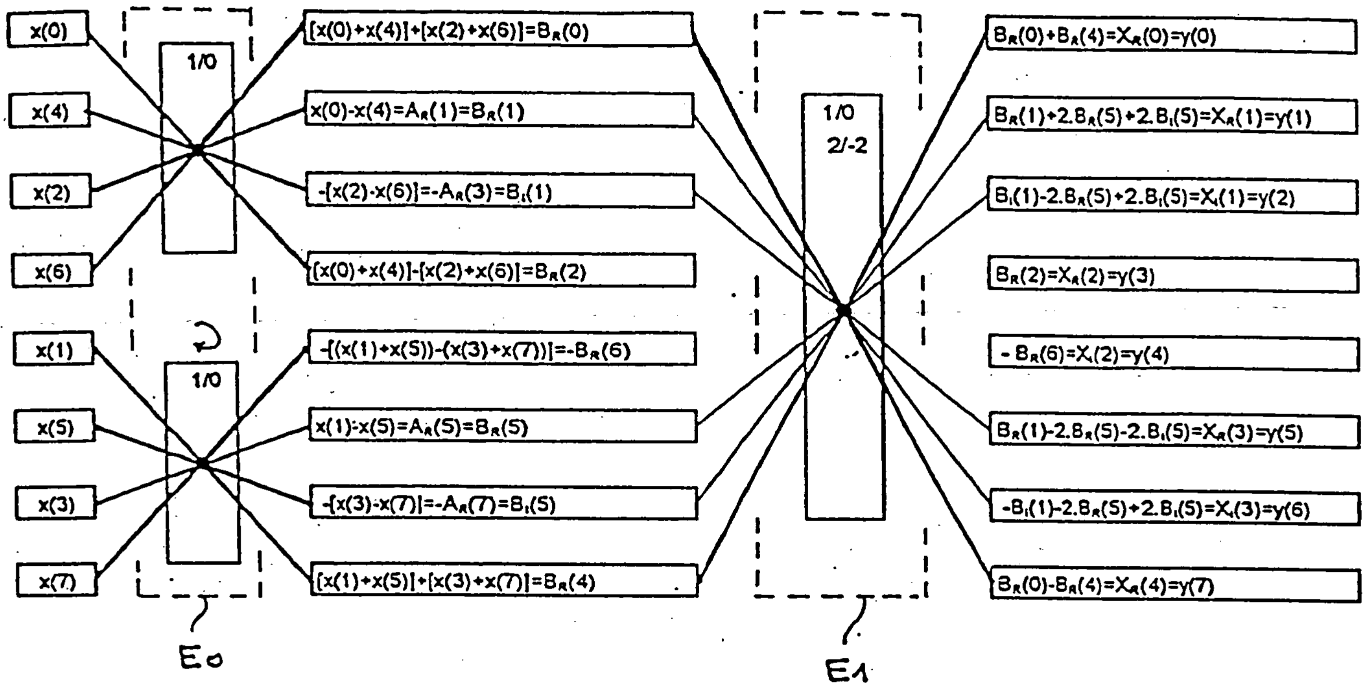

x(0)
x(4)
x(2)
x(6)
x(1)
x(5)
x(3)
x(7)
1/0
1/0
[x(0)+x(4)]+[x(2)+x(6)]=B_R(0)
x(0)-x(4)=A_R(1)=B_R(1)
-[x(2)-x(6)]=-A_R(3)=B_I(1)
[x(0)+x(4)]-[x(2)+x(6)]=B_R(2)
-[(x(1)+x(5))-(x(3)+x(7))]=-B_R(6)
x(1)-x(5)=A_R(5)=B_R(5)
-[x(3)-x(7)]=-A_R(7)=B_I(5)
[x(1)+x(5)]+[x(3)+x(7)]=B_R(4)
1/0
2/-2
B_R(0)+B_R(4)=X_R(0)=y(0)
B_R(1)+2.B_R(5)+2.B_I(5)=X_R(1)=y(1)
B_I(1)-2.B_R(5)+2.B_I(5)=X_I(1)=y(2)
B_R(2)=X_R(2)=y(3)
- B_R(6)=X_I(2)=y(4)
B_R(1)-2.B_R(5)-2.B_I(5)=X_R(3)=y(5)
-B_I(1)-2.B_R(5)+2.B_I(5)=X_I(3)=y(6)
B_R(0)-B_R(4)=X_R(4)=y(7)
E0
E1

FIG.16

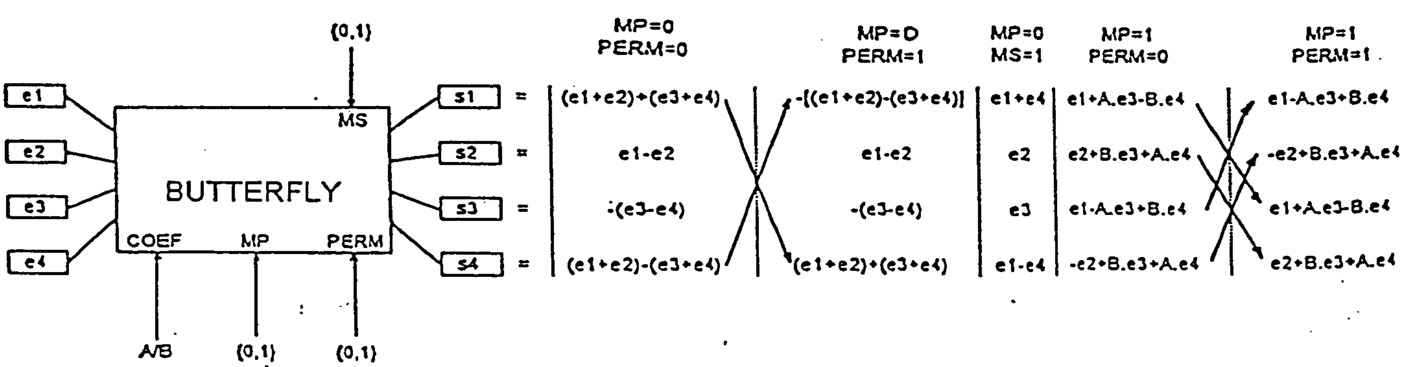

(0,1)
e1
e2
e3
e4
MS
BUTTERFLY
COEF
MP
PERM
A/B
(0,1)
(0,1)
s1 =
s2 =
s3 =
s4 =
MP=0
PERM=0
(e1+e2)+(e3+e4)
e1-e2
-(e3-e4)
(e1+e2)-(e3+e4)
MP=0
PERM=1
-[(e1+e2)-(e3+e4)]
e1-e2
-(e3-e4)
(e1+e2)+(e3+e4)
MP=0
MS=1
e1+e4
e2
e3
e1-e4
MP=1
PERM=0
e1+A.e3-B.e4
e2+B.e3+A.e4
e1-A.e3+B.e4
-e2+B.e3+A.e4
MP=1
PERM=1
e1-A.e3+B.e4
-e2+B.e3+A.e4
e1+A.e3-B.e4
e2+B.e3+A.e4

FIG.17

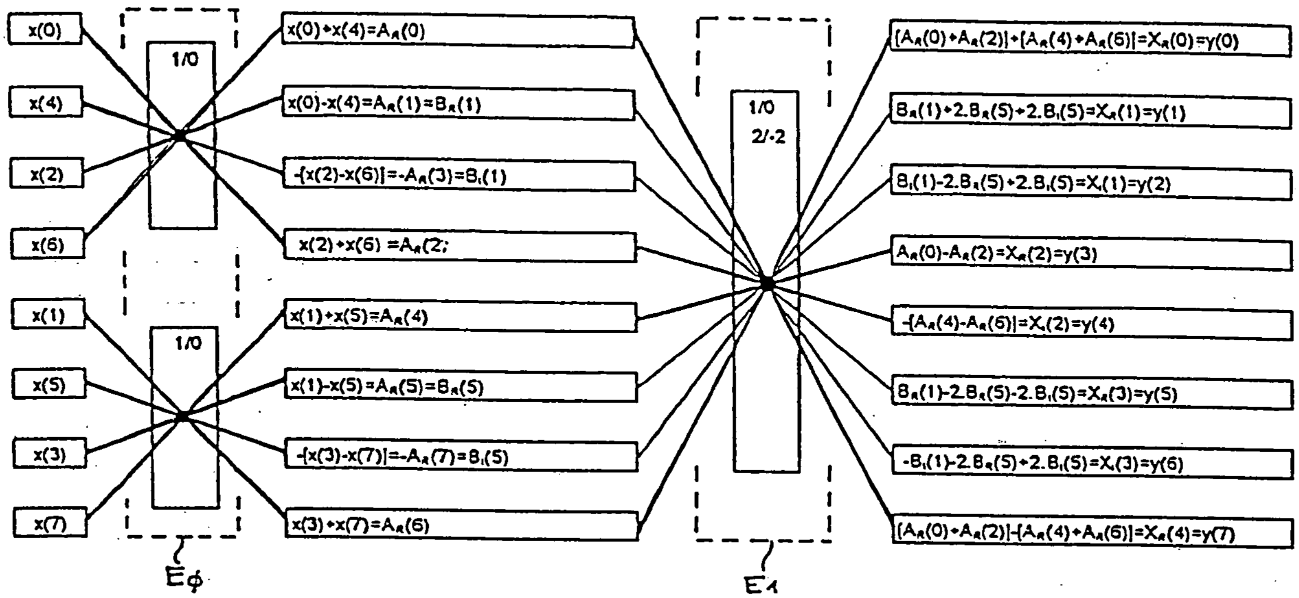

x(0)
x(4)
x(2)
x(6)
x(1)
x(5)
x(3)
x(7)
1/0
1/0
x(0)+x(4)=A_R(0)
x(0)-x(4)=A_R(1)=B_R(1)
-[x(2)-x(6)]=-A_R(3)=B_I(1)
x(2)+x(6)=A_R(2)
x(1)+x(5)=A_R(4)
x(1)-x(5)=A_R(5)=B_R(5)
-[x(3)-x(7)]=-A_R(7)=B_I(5)
x(3)+x(7)=A_R(6)
1/0
2/-2
[A_R(0)+A_R(2)]+[A_R(4)+A_R(6)]=X_R(0)=y(0)
B_R(1)+2.B_R(5)+2.B_I(5)=X_R(1)=y(1)
B_I(1)-2.B_R(5)+2.B_I(5)=X_I(1)=y(2)
A_R(0)-A_R(2)=X_R(2)=y(3)
-[A_R(4)-A_R(6)]=X_I(2)=y(4)
B_R(1)-2.B_R(5)-2.B_I(5)=X_R(3)=y(5)
-B_I(1)-2.B_R(5)+2.B_I(5)=X_I(3)=y(6)
[A_R(0)+A_R(2)]-[A_R(4)+A_R(6)]=X_R(4)=y(7)
Eφ
E1

FIG.18

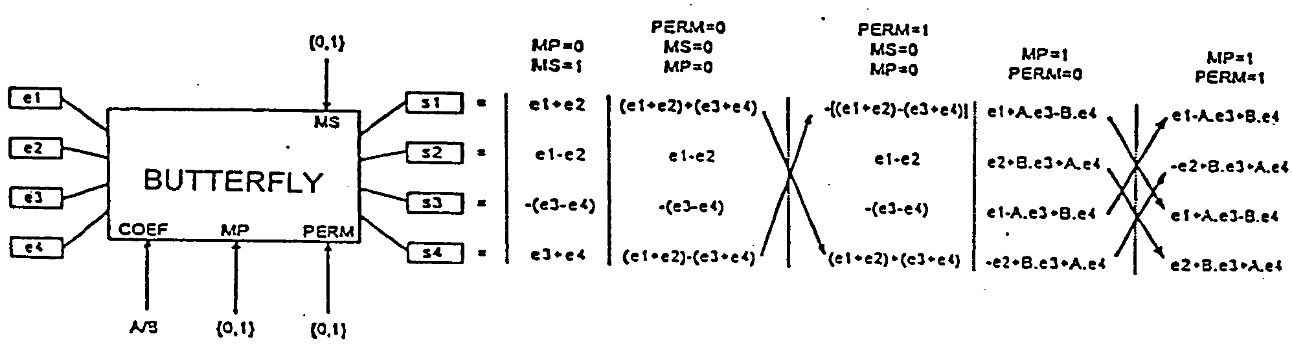

{0,1}
MS
e1
e2
e3
e4
BUTTERFLY
COEF
MP
PERM
A/B
{0,1}
{0,1}
s1 =
s2 =
s3 =
s4 =
MP=0
MS=1
e1+e2
e1-e2
-(e3-e4)
e3+e4
PERM=0
MS=0
MP=0
(e1+e2)+(e3+e4)
e1-e2
-(e3-e4)
(e1+e2)-(e3+e4)
PERM=1
MS=0
MP=0
-[(e1+e2)-(e3+e4)]
e1-e2
-(e3-e4)
(e1+e2)+(e3+e4)
MP=1
PERM=0
e1+A.e3-B.e4
e2+B.e3+A.e4
e1-A.e3+B.e4
-e2+B.e3+A.e4
MP=1
PERM=1
e1-A.e3+B.e4
-e2+B.e3+A.e4
e1+A.e3-B.e4
e2+B.e3+A.e4

FIG.19

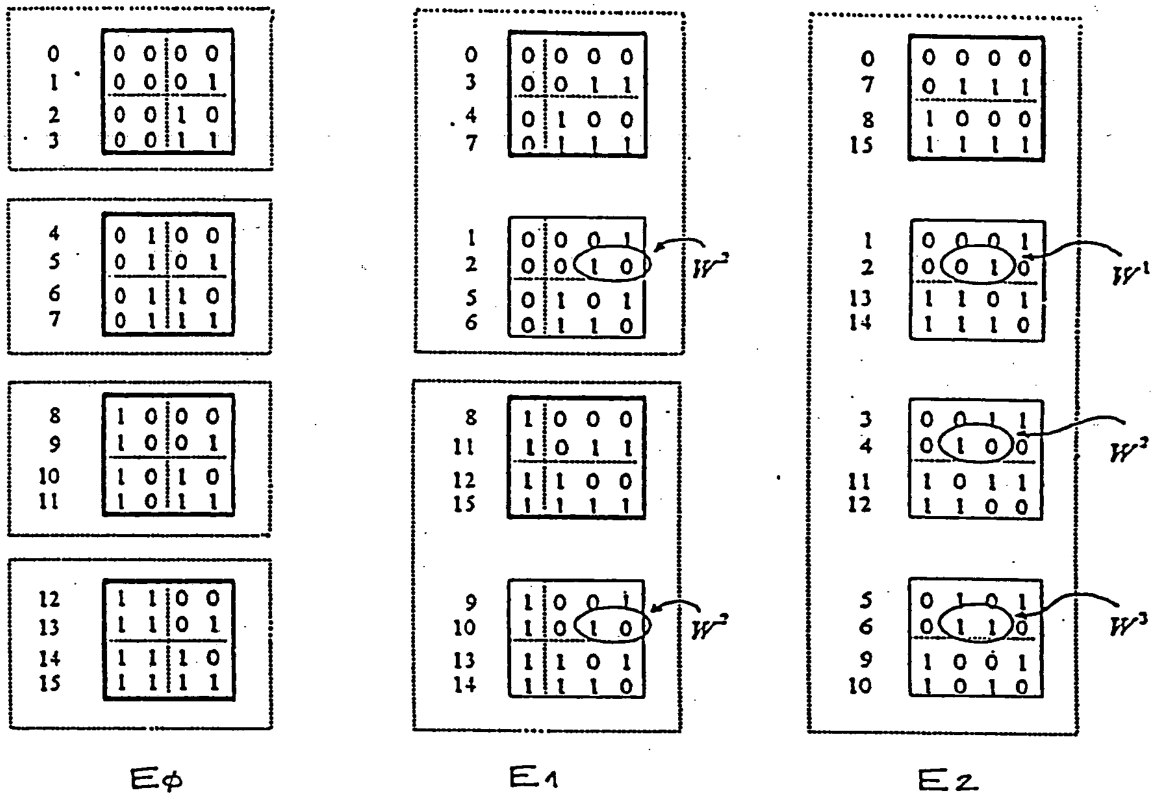


FIG. 20

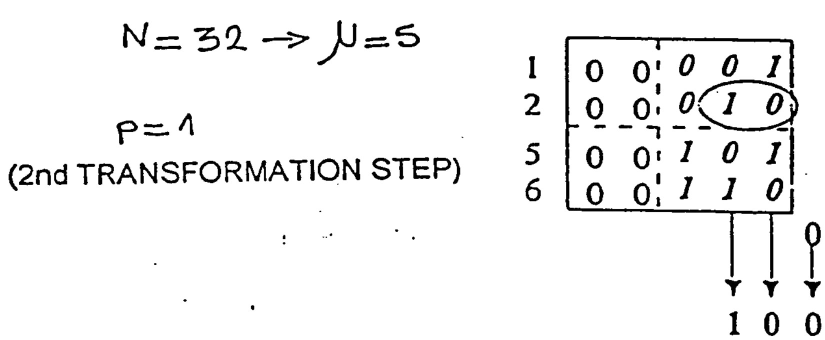


FIG. 21

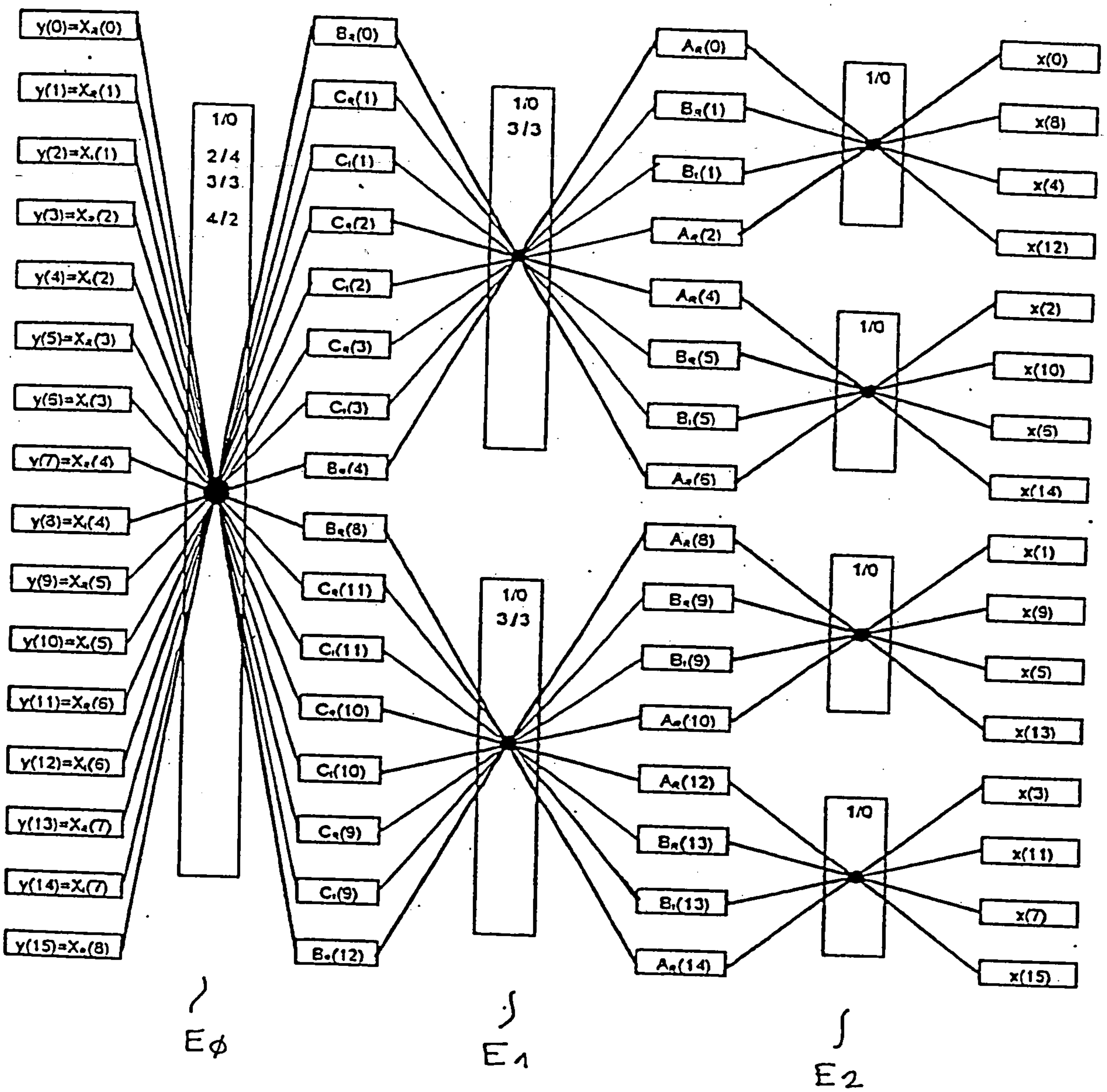

1/0
2/4
3/3
4/2
1/0
3/3
1/0
3/3
1/0
1/0
1/0
1/0
x(0)
x(8)
x(4)
x(12)
x(2)
x(10)
x(6)
x(14)
x(1)
x(9)
x(5)
x(13)
x(3)
x(11)
x(7)
x(15)
Eφ
E1
E2

FIG.22

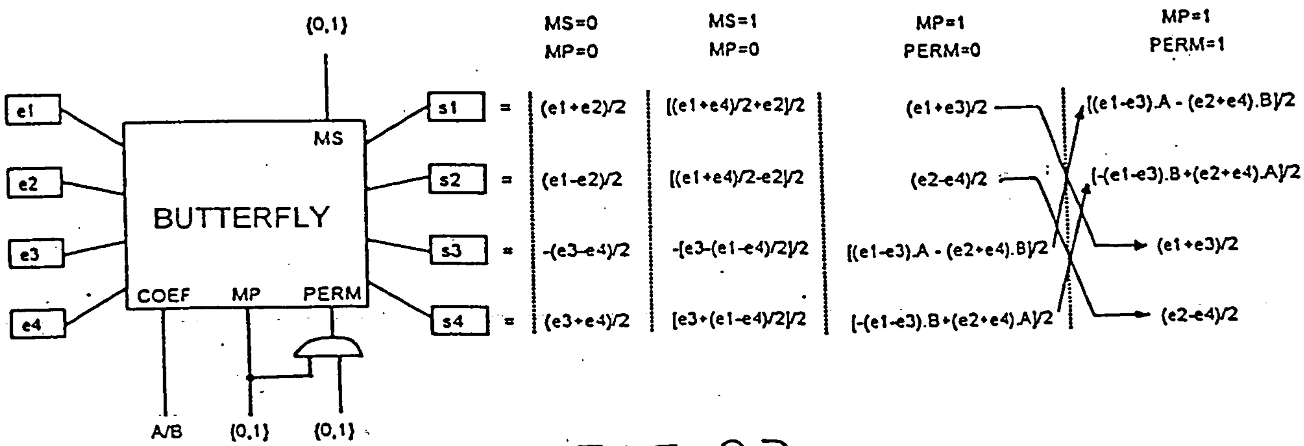

{0,1}
MS=0
MP=0
MS=1
MP=0
MP=1
PERM=0
MP=1
PERM=1
e1
e2
e3
e4
MS
BUTTERFLY
COEF
MP
PERM
s1
s2
s3
s4
(e1+e2)/2
(e1-e2)/2
-(e3-e4)/2
(e3+e4)/2
[(e1+e4)/2+e2]/2
[(e1+e4)/2-e2]/2
-[e3-(e1-e4)/2]/2
[e3+(e1-e4)/2]/2
(e1+e3)/2
(e2-e4)/2
[(e1-e3).A - (e2+e4).B]/2
[-(e1-e3).B+(e2+e4).A]/2
A/B
{0,1}
{0,1}

FIG. 23

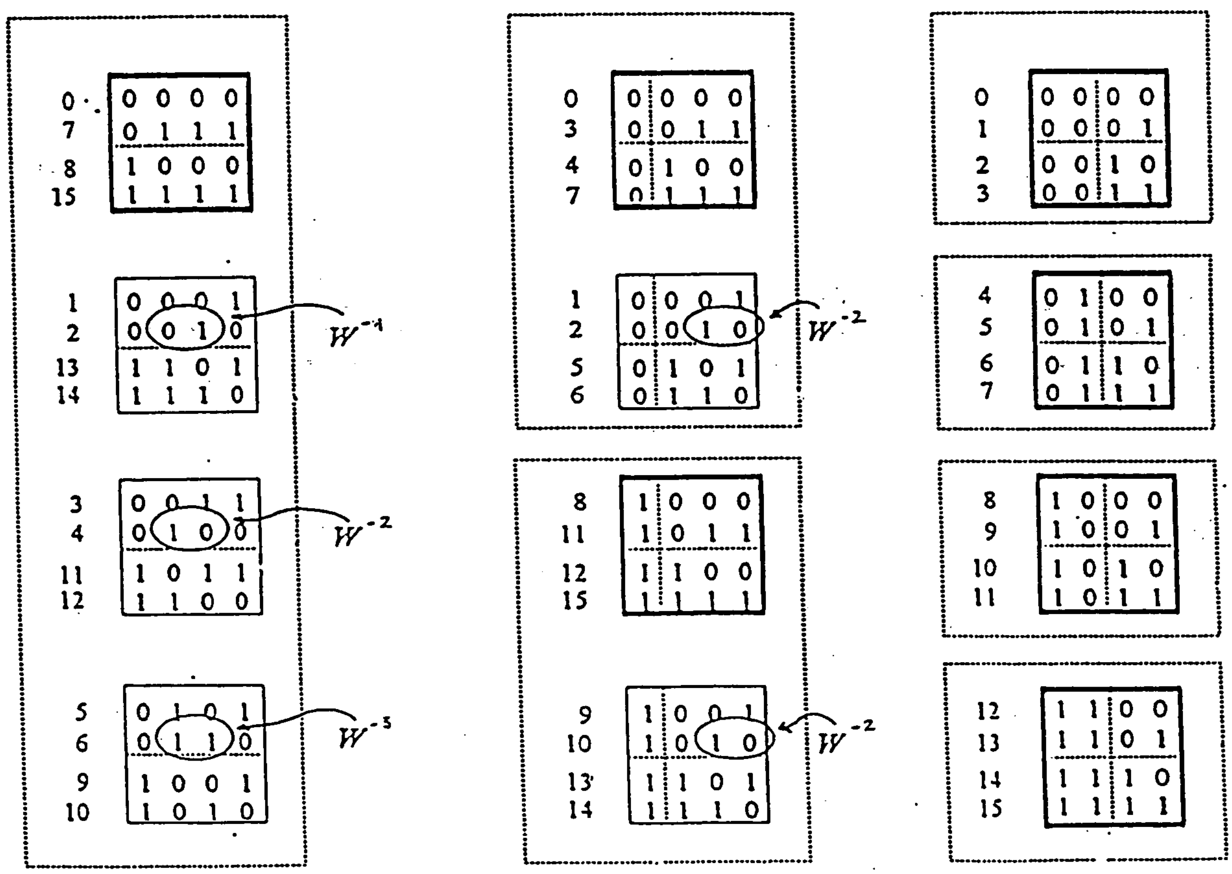

W^{-1}
W^{-2}
W^{-3}
W^{-2}
W^{-2}

FIG. 24